CMPE - MS

Selected Curriculum for

Interest in VLSI Advising Sheet

Computer Engineering master's students are required to take one course from each of the four groups listed below.

Group "A"		Group "C"	
☐ CSCE 5510 Wireless Communications		☐ CSCE 5440 Real-Time Software Development	
☐ CSCE 5520 Wireless Networks and Protocols		CSCE 5612 (5933) Embedded Hardware & Software Design	
☐ CSCE 5580 Computer Networks		☐ CSCE 5620 Real-Time Operating Systems	
Group "B"	Grou	ıp "D"	
☐ CSCE 5160 Parallel Processing and Algorithms		☐ CSCE 5730 Digital CMOS VLSI Design	
☐ CSCE 5610 Computer System Architecture		☐ CSCE 5740 Topics in Modern Electronic System Design	
☐ CSCE 5640 Operating System Design		☐ CSCE 5760 Design for Fault Tolerance	
Suggested Courses:			
□ CSCE 5510	Wireless Communication	ons	3 sch
☐ CSCE 5520		Wireless Networks and Protocols	
☐ CSCE 5540		Introduction to Sensor Networks	
□ CSCE 5580		Computer Networks	
□ CSCE 5610	•	Computer System Architecture	
☐ CSCE 5612 (5933		Embedded Hardware and Software Design	
☐ CSCE 5615 (593	•	Networks-on-Chip	
□ CSCE 5620	Real-Time Operating Sy	Real-Time Operating Systems	
☐ CSCE 5640	Operating System Design	Operating System Design	
☐ CSCE 5730	Digital CMOS VLSI Design	Digital CMOS VLSI Design	
☐ CSCE 5740	Topics in Modern Elect	Topics in Modern Electronic System Design	
☐ CSCE 5760	Design for Fault Tolera	Design for Fault Tolerance	
☐ CSCE 6731	Advanced Topics in VLS	Advanced Topics in VLSI Systems	
Major Professors Comments/Sug	gestions:		
			
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- For MS with thesis, the total number of hours required is 30.
- For MS without thesis, the total number of hours required is 36.
- To continue in good standing, a student must maintain a 3.0 GPA overall.
- Only one organized course (not less than 3 sch), and up to 2 Internship courses (max 2 each) may be placed on the MS degree plan. All outside courses must have prior approval by the student's major professor with a justification written on the back of the degree plan.