Eric Strong, P.E., Ph.D.

(c) 972.238.8911, eric.strong@untdallas.edu

Education

Doctor of Philosophy, Electrical Engineering, Stanford University, Stanford, California

Master of Science, Statistics, Stanford University, Stanford, California

Master of Science, Electrical Engineering, California Institute of Technology, Pasadena, California

Bachelor of Science, Electrical Engineering and Applied Mathematics, US Air Force Academy, CO

Professional Certifications and Appointments

Texas Educator Certificate, Standard, Physical Science/Mathematics/Engineering Grades (8-12) Physical Science/Mathematics/Engineering Highly Qualified Teacher (TExES score: 295 of 300)

Adjunct Faculty, Richland College; Licensed Professional Engineer, State of Colorado

Teaching Experience

School for the Talented and Gifted (TAG Magnet), Dallas ISD (Fall 2010 - current)

- Dallas ISD Top 100 Teacher (2015)
- TAG Magnet Teacher of the Year (2012 2013)
- AP Physics 1 and AP Physics 2 (Fall 2014 Current)
- AP Physics B and PreAP Physics (Fall 2010 Spring 2014)
 - May 2011 AP Physics B examination has School's highest pass rate in six years.
 - May 2012 AP Physics B examination has School's highest pass rate in nine years.
 - o Increased 11th grade science TAKS commended rate from 67.2% to 83.9%, 2010 to 2011.
 - o Increased 11th grade science TAKS commended rate to 98.5% for 2012
- Fast Track Mathematics (PreAP Algebra II and PreAP Precalculus in one year)
 - o 100% of students commended on 10th grade TAKS test (2010 and 2011)
 - o 100% of students reach "Advanced Academic" (level 3) on STAAR Algebra 2 EOC
- Digital Fundamentals (CETT 1425)/Richland College dual credit

Berkner High School/STEM Academy, Richardson ISD (Fall 2008 - Spring 2010)

- PreAP Precalculus (2009-10)
- Project Lead The Way/Richland College dual credit Digital Electronics (2009-10)
- Project Lead The Way Principles of Engineering (2008-09)

Richland College, Adjunct Faculty (Spring 2006 - current)

- DC Circuits (CETT1403), Spring 2007
- AC Circuits (CETT1405), Fall 2006, Fall 2007, Spring 2008
- Solid State Devices (CETT1429), Spring 2008
- Linear Integrated Circuits (CETT1457), Spring 2006

Professional Training

AP Summer Training Institute, Physics B/C (Summer 2010, Summer 2013)

AP Summer Training Institute, Calculus BC (Summer 2009)

Project Lead the Way Summer Training Institute, Digital Electronics (Summer 2009)

Project Lead the Way Summer Training Institute, Principles of Engineering (Summer 2008)

Work Experience

Cadence Design Systems (7 years) - Application Engineering, Application Engineering Development and Support, Technical Marketing and Product Engineering, Product Launch Team management.

E-Systems (5 years) - Senior Specialist for Research and Development, Program Manager

US Air Force, Active Duty (5 years) - Flight Test Engineering, Information Engineering

US Air Force, Reserve (15 years) - Information Engineering