FACULTY CURRICULUM VITAE

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AREA OF EXPERTISE

Computer Science: Analysis of Algorithms, design; Network and Telecom; Data and network Security, Information science; Computer Architecture and design

EDUCATION

Year	Degree	Major	Institution
2006	MBA	Entrepreneurship	UT Dallas
1998	Ph.D.	Computer Science	Univ. So. Florida

PROFESSIONAL EXPERIENCE

Begin/End Year	Place of Employment	Rank / Job Title
2007 -	Truesigna Systems Inc.	M. Director
1998- 2007	Alcatel-Lucent	various roles (sr. mgmt)

SCHOLARSHIP

Sample journal publications:

1. Bandwidth sensitive fast failure recovery scheme for Metro Ethernet. Computer Networks 52(8): 1603-1616 (2008) with P. Nair et. al.
2. Traffic Engineering in Metro Ethernet, International Journal of Computer Networks: 101-114, with P. Nair et. al.
3. Traffic engineering in metro Ethernet. IEEE Network 19(2): 10-17 (2005), with M. Ali et. Al.
4. Issues and Approaches on Extending Ethernet Beyond LANs, IEEE Comm. Magazine, 40-47, Mar. 2004
5. VBR video traffic management using a predictor-based architecture. Computer Communications 23(1): 62-70 (2000) with R. Sankar, N. Ranganathan
6. Adaptive VBR Video Trac Management for Higher Utilization of ATM, ACM SIGCOMM, 35-42, July 1998 with R. Sankar, N. Ranganathan

Book chapter:

7. G. Chiruvolu, R. Sankar, and K. Christensen, **Short-Term Prediction of Traffic for Improved ATM QoS**, *in The 6th International Workshop on Network and Operating Systems Support for Digital Audio and Video (NOSSDAV 96)*, Springer-Verlag, 1996.

With over **40+** refereed papers in IEEE conference publications and whitepapers.