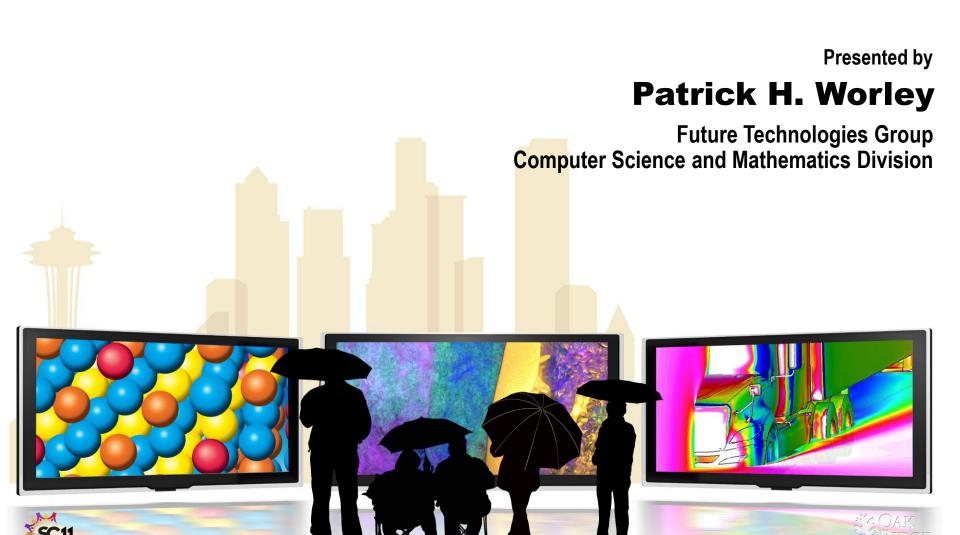
Performance Evaluation and Analysis Consortium (PEAC) End Station



Overview

The PEAC End Station provides the performance evaluation and performance tool developer communities access to the Leadership Computing Facility (LCF) systems

Consortium goals	
System evaluation	 Evaluate the performance of LCF systems using standard and custom micro-, kernel, and application benchmarks
	 Determine efficient usage techniques and identify and characterize system performance issues
Performance tools	 Port performance-related tools and system infrastructure to LCF systems and make them available to Oak Ridge Leadership Computing Facility (OLCF) and Argonne Leadership Class Facility (ALCF) users
	 Further develop the tools to take into account the scale and unique features of LCF systems
Performance modeling	 Validate the effectiveness of and further develop performance characterization and prediction methodologies for LCF systems



Overview (continued)

Consortium goals (continued)

Application analysis and optimization

 Analyze and help optimize performance of current and candidate LCF application codes

Performance and application community support

- Provide access to other performance researchers who are interested in contributing to the performance evaluation of the LCF systems or in porting complementary performance tools of use to the OLCF user community
- Provide access to application developers who wish to evaluate the performance of their codes on LCF systems

All of this must be accomplished while adhering to the "Golden Rules" of the PEAC community:

- Low visibility (no production runs!)
- Open and fair evaluations
- Timely reporting of results



Status as of September 1, 2011

23 active ALCF users, 40 active OLCF users

Working on

- Application performance analysis and optimization (18)
- Application performance modeling (10)
- Algorithm development (7)
- Performance tool development (37)
- Performance-related system infrastructure development (16)
- System performance evaluation (10)

many working in multiple areas

Consuming

- XT5: 7,000,000 processor hours out of an allocation of 20,000,000 hours
- BG/P: 2,000,000
 processor hours out of an allocation of 10,000,000 hours

since January 1, 2011

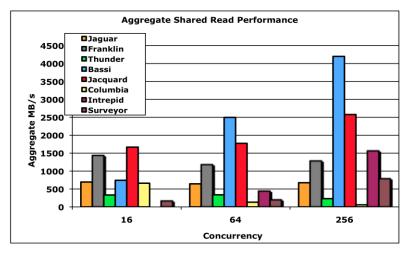
Contributing to (at least)

- Two book chapters
- Four journal articles
- Eighteen proceedings papers, including: 2 CUG, 1 Euro-Par, 1 ICPP, 3 ICS, 3 IPDPS, 5 SC11
- Two oral presentations
- Two code releases

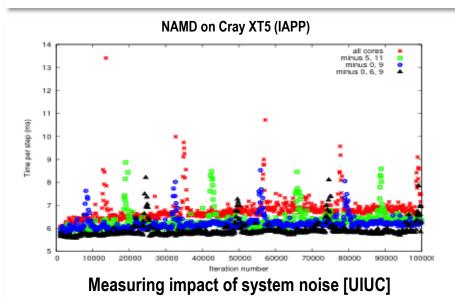
appearing since January 1, 2011

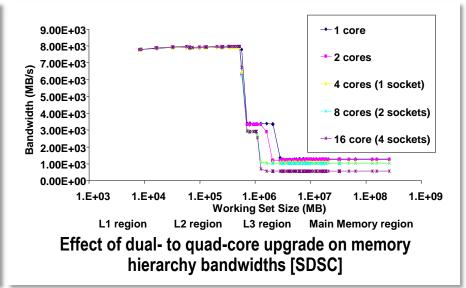


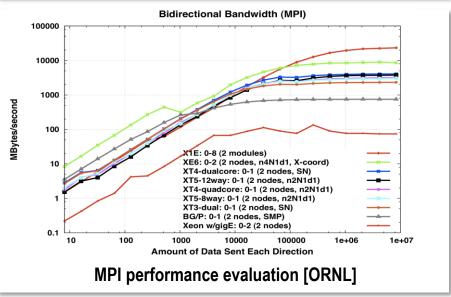
System evaluation



I/O performance characterization [LBL]

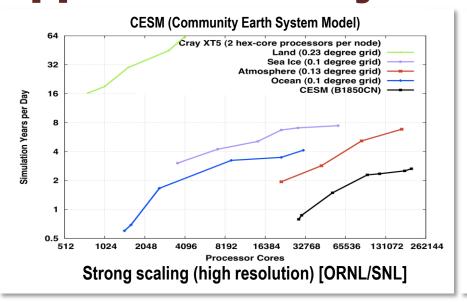


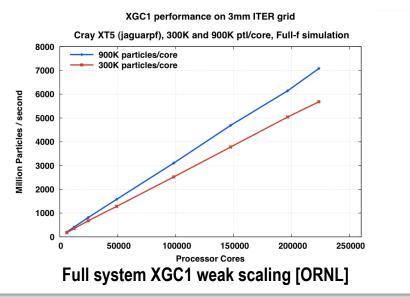


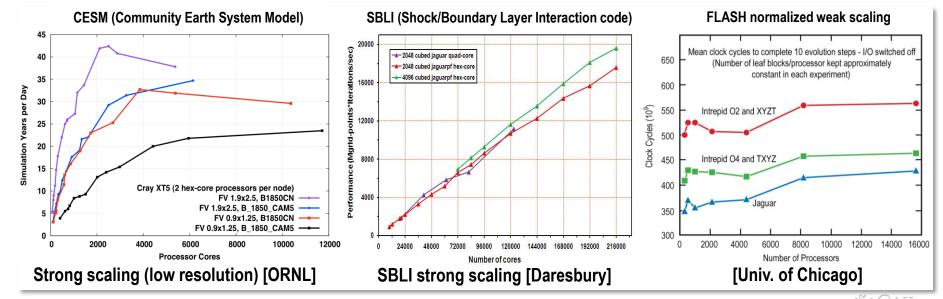




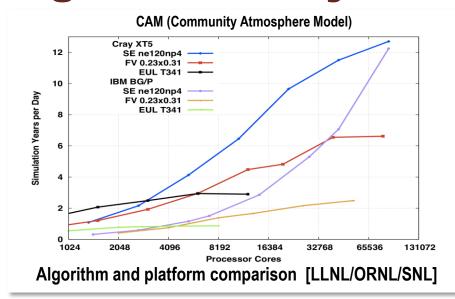
Application analysis and evaluation

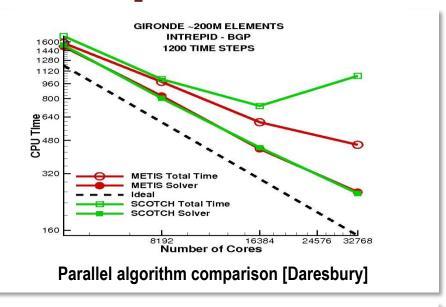




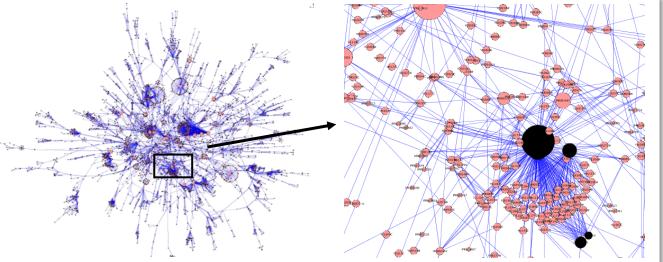


Algorithm analysis and comparison



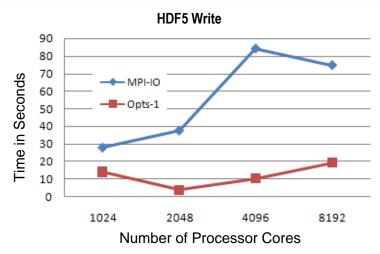


Processing of genomes into domain maps: need improved load balancing that takes into account scale-free nature of the graphs [RENCI, NCSA]

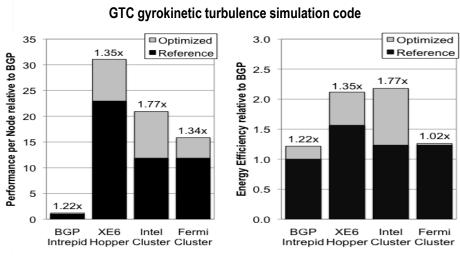




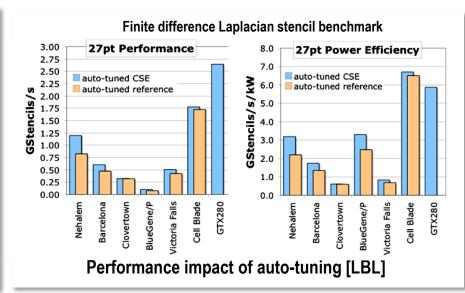
Optimization and optimization technology

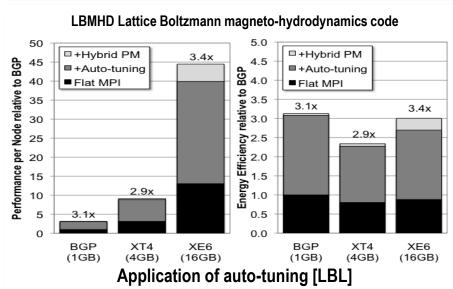


I/O optimization in PFLOTRAN [NCSU]



Optimizing for CPUs and GPUs [LBL]

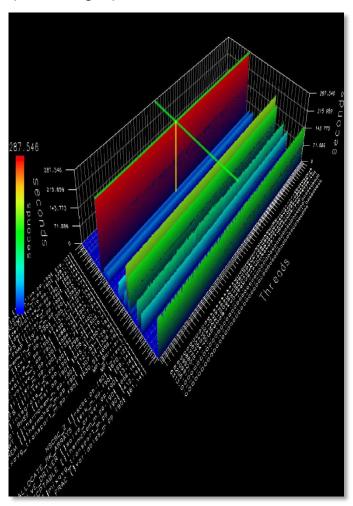




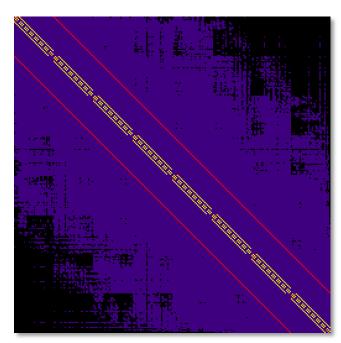


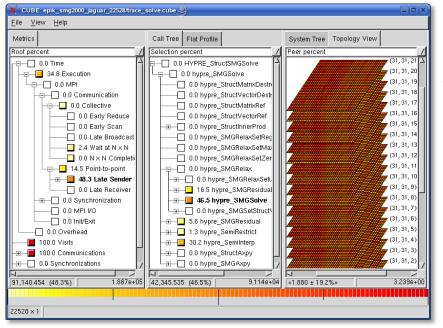
Tool development

TAU graphical display of profile data by thread (Univ. Oregon)



Visualization of point-to-point communication topology collected with mpiP (LLNL, ORNL)

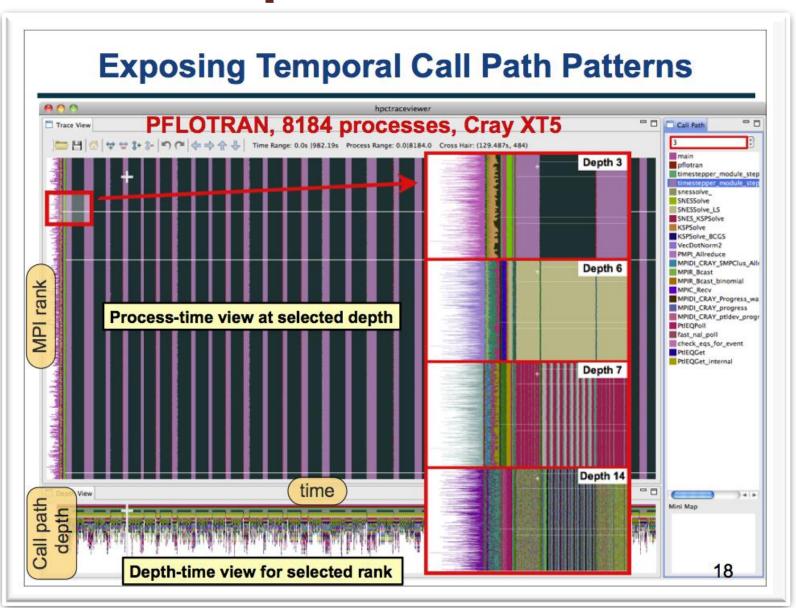




SCALASCA trace-based performance analysis (FZ-Jülich, Univ. Tenn.)



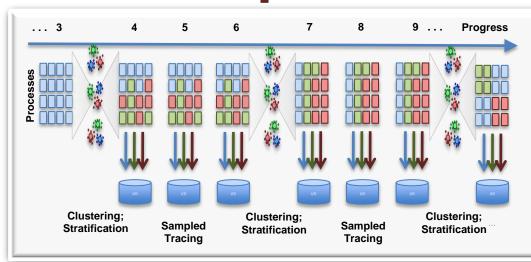
Tool development



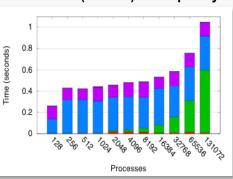
Sample-based tracing and visualization using HPCToolkit [Rice]



Tool development

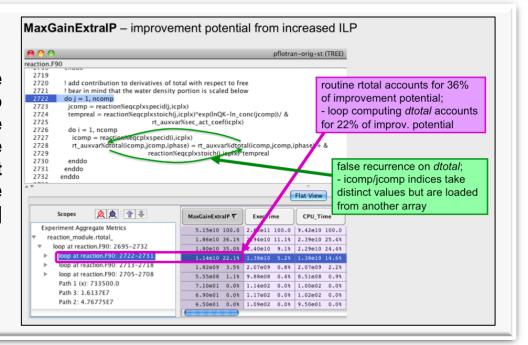


Using Clustering Algorithm with Parallel Extensions to K-Medoids (CAPEK) to adaptively sample performance



traces (improving performance tracing at scale) [LLNL, RENCI, Microsoft]

Using performance modeling to predict where performance optimization might be most effective [SDSC, ORNL]





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