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Mr. Anderson is a professor of security engineering at Cambridge University. He cofounded the modern study of security economics, which has had much success in explaining system failure in terms of microeconomic analysis. He has also been involved for many years in technical security research, being a leading expert on attacks on automatic teller machines, other card payment systems, and hardware tamper-resistance in general. Previously, he worked in banking and consultancy. He is a fellow of the Institution of Engineering and Technology and chairs the Foundation for Information Policy Research.