

UNITED STATES INTERNATIONAL TRADE COMMISSION
Washington, D.C. 20436

In the Matter of

**CERTAIN SEMICONDUCTOR CHIPS WITH
MINIMIZED CHIP PACKAGE SIZE AND
PRODUCTS CONTAINING SAME**

Inv. No. 337-TA-605

**NOTICE OF COMMISSION DETERMINATION NOT TO REVIEW AN INITIAL
DETERMINATION EXTENDING THE TARGET DATE AND SETTING A DATE FOR
HEARING**

AGENCY: U.S. International Trade Commission.

ACTION: Notice.

SUMMARY: Notice is hereby given that the U.S. International Trade Commission has determined not to review the presiding administrative law judge's ("ALJ") initial determination ("ID") (Order No. 53), extending the target date in the above captioned investigation to February 21, 2009, and setting a date for hearing.

FOR FURTHER INFORMATION CONTACT: Megan M. Valentine, Office of the General Counsel, U.S. International Trade Commission, 500 E Street, S.W., Washington, D.C. 20436, telephone (202) 708-2301. Copies of non-confidential documents filed in connection with this investigation are or will be available for inspection during official business hours (8:45 a.m. to 5:15 p.m.) in the Office of the Secretary, U.S. International Trade Commission, 500 E Street, S.W., Washington, D.C. 20436, telephone (202) 205-2000. General information concerning the Commission may also be obtained by accessing its Internet server at <http://www.usitc.gov>. The public record for this investigation may be viewed on the Commission's electronic docket (EDIS) at <http://edis.usitc.gov>. Hearing-impaired persons are advised that information on this matter can be obtained by contacting the Commission's TDD terminal on (202) 205-1810.

SUPPLEMENTARY INFORMATION: The Commission instituted this investigation on May 21, 2007, based on a complaint filed by Tessera, Inc. of San Jose, California against Spansion, Inc. and Spansion, LLC, both of Sunnyvale, California; QUALCOMM, Inc. of San Diego, California; AT1 Technologies of Thornhill, Ontario, Canada; Motorola, Inc. of Schaumburg, Illinois; STMicroelectronics N.V. of Geneva, Switzerland; and Freescale Semiconductor, Inc. of Austin, Texas (collectively "Respondents"). *72 Fed. Reg.* 28522 (May 21, 2007). The complaint alleges violations of section 337 in the importation into the United States, the sale for

importation, and the sale within the United States after importation of certain semiconductor chips with minimized chip package size or products containing same by reason of infringement of one or more of claims 1, 2, 6, 12, 16-19, 21, 24-26, and 29 of U.S. Patent No. 5,852,326 (“the ‘326 patent”), and claims 1-11, 14, 15, 19, and 22-24 of US. Patent No. 6,433,419 (“the ‘419 patent”).

On February 26, 2008, the ALJ issued an order (Order No. 52), granting Respondents’ motion for stay pending reexamination of the ‘326 patent and the ‘419 patent. On April 29, 2008, the Commission issued a notice and order to review and reverse Order No. 52, thus requiring the ALJ to reschedule the evidentiary hearing.

On May 13, 2008, the ALJ issued the subject ID under Commission rule 210.51(a) (19 C.F.R. § 210.51(a)), extending the target date to February 20, 2009 and setting a new date for the evidentiary hearing. No petitions for review of this ID were filed.

The Commission has determined not to review the ID.

The authority for the Commission’s determination is contained in section 337 of the Tariff Act of 1930, as amended (19 U.S.C. § 1337), and in section 210.42 of the Commission’s Rules of Practice and Procedure (19 C.F.R. § 210.42).

By order of the Commission.

/s/
Marilyn R. Abbott
Secretary to the Commission

Issued: June 4, 2008