

**UNITED STATES INTERNATIONAL TRADE COMMISSION**  
**Washington, D.C. 20436**

**In the Matter of**

**CERTAIN SEMICONDUCTOR CHIPS WITH  
MINIMIZED CHIP PACKAGE SIZE AND  
PRODUCTS CONTAINING SAME (III)**

**Investigation No. 337-TA-630**

**NOTICE OF COMMISSION DETERMINATION NOT TO REVIEW AN INITIAL  
DETERMINATION FINDING TWO RESPONDENTS IN DEFAULT**

**AGENCY:** U.S. International Trade Commission.

**ACTION:** Notice.

**SUMMARY:** Notice is hereby given that the U.S. International Trade Commission has determined not to review the presiding administrative law judge's ("ALJ") initial determination ("ID") (Order No. 46) in this investigation finding respondents TwinMOS Technologies Inc. and TwinMOS Technologies USA Inc. in default.

**FOR FURTHER INFORMATION CONTACT:** Panyin A. Hughes, Office of the General Counsel, U.S. International Trade Commission, 500 E Street, S.W., Washington, D.C. 20436, telephone (202) 205-3042. Copies of non-confidential documents filed in connection with this investigation are or will be available for inspection during official business hours (8:45 a.m. to 5:15 p.m.) in the Office of the Secretary, U.S. International Trade Commission, 500 E Street, S.W., Washington, D.C. 20436, telephone (202) 205-2000. General information concerning the Commission may also be obtained by accessing its Internet server (<http://www.usitc.gov>). The public record for this investigation may be viewed on the Commission's electronic docket (EDIS) at <http://edis.usitc.gov>. Hearing-impaired persons are advised that information on this matter can be obtained by contacting the Commission's TDD terminal on (202) 205-1810.

**SUPPLEMENTARY INFORMATION:** This investigation was instituted on January 14, 2008, based on a complaint filed by Tessera, Inc. of San Jose, California ("Tessera") on December 21, 2007, and supplemented on December 28, 2007. *73 Fed. Reg.* 2276 (January 14, 2008). The complaint alleges violations of section 337 of the Tariff Act of 1930 (19 U.S.C. § 1337) in the importation into the United States, the sale for importation, and the sale within the United States after importation of certain semiconductor chips with minimized chip package size or products containing same by reason of infringement of various claims of United States Patent Nos. 5,663,106; 5,679,977; 6,133,627; and 6,458,681. The complaint names eighteen respondents.

On February 19, 2008, Tessera filed a motion for an order to show cause and default against two respondents: TwinMOS Technologies Inc. and TwinMOS Technologies USA Inc. (collectively “TwinMOS”). On March 12, 2008, the ALJ issued Order No. 12 ordering those respondents to show cause why they should not be found in default for failing to respond to the complaint and notice of investigation.

On August 28, 2009, the ALJ issued the subject ID finding TwinMOS in default. No petitions for review of this ID were filed. The Commission has determined not to review the ID.

The authority for the Commission’s determination is contained in section 337 of the Tariff Act of 1930, as amended (19 U.S.C. § 1337), and in section 210.42 of the Commission’s Rules of Practice and Procedure (19 C.F.R. § 210.42).

By order of the Commission.

/s/

Marilyn R. Abbott  
Secretary to the Commission

Issued: September 15, 2009