Subject: OFFICIAL COMMENT: SHAMATA
From: Orhun Kara <orhun@uekae.tubitak.gov.tr>
Date: Wed, 21 Jan 2009 09:00:54 +0200
To: hash-function@nist.gov
CC: hash-forum@nist.gov

Dear All,

The performance results of a new 8-bit implementation of SHAMATA can be
found in its updated spec document. It is an assembly iplementation and
occupies roughly 400 B of RAM with 1.5 kB for code size. The throughput is
3 kB/s on the Atmel AT89C51ED2 micro-controller with a 33 MHz frequency.

The details are on the updated specification document which is available
on the web page of SHAMATA:
http://www.uekae.tubitak.gov.tr/home.do?ot=1&sid=601&pid=547
<http://www.uekae.tubitak.gov.tr/home.do?ot=1&sid=601&pid=547>

Best regards,
Orhun