194	40 19	50 I	960 19	970 198	30 19	990
KEY FIGURES						
(PHOTOS SELECTED ON BASIS OF AVAILABILITY)	J. Presper Eckert J. Von Neumann	Grace Hopper Gordon Moore	Paul Baran Doug Englebart JR Licklider	Alan Shugart Vincent Cerf Charles Simon	i Seymour Cray Tim Berners-Lee	Leonard Adelman
1 - 1890 Automated punch-card machine (H Hollerith) used in U.S. census; Hollerith's firm merges with others, becoming IBM in 1924 SOFTWARE	2 - 1942. Machines to decrypt German, Japanese codes (U.S. Navy Computing Machine Lab, NCR) 3 - 1944. Harvard Mark I; weighs 5 tons 4 - 1945. Electronic Numerical Integrator and Computer (ENIAC), (J Eckert, J Mauchley, U Penn); computes ballistic firing tables; 19,000 vacuum tubes 1 - 1940s. First ENIAC instructions typed manually by 100 Navy women in war effort 2 - 1940s -> Physics, mathematics of signal processing -	 1950 Standards Eastern/Western Automatic Computers (SEAC, SWAC), electronic stored-program machines, built for DoD (National Bureau of Standards) 1951 Electronic Discrete Variable Automatic Calculator (EDVAC), stored-program unit (ENIAC team, J von Neumann) for Army ballistics calculations 1951 Whirtwind computer (MIT) for flight simulation. Vectorscope graphics display; random-access, magnetic- drum core memory 1951 Univoc I (ENIAC developers, Remington Rand) 	 SWAC), electronic stored-program machines, built D (National Bureau of Standards) Electronic Discrete Variable Automatic Calculator AC), stored-program unit (ENIAC team, J von ann) for Army ballistics calculations. Whirdwind computer (MIT) for flight simulation. rscope graphics display; random-access, magnetic- 	(Xerox PARC) 19 - 1976 Croy-1 vedor machine (133 Mflops) to LLNL 20 - 1976 Apple 1 sells as a kit 21 - 1977 Apple Computer Co (S Jobs, S Wozniak). Apple II with color graphics in stores 22 - 1977 Microsoft Corp (P Allen, B Gates) 23 - late 70s -> Rise of personal computer: MITS Altair, Radio Shack TRS-30, Commodore PET and -64, Digital	27 - 1982 SUN Microsystems (for Stantord University Network) (Scott McNealy, Bill Joy, others) 28 - 1982 Cray X-MP, with multiprocessor architecture 29 - 1984 Anale Marclansch	34 - 1992 Multiprocessor Cray C 35 - 1994 First "Beowulf" cluster 36 - 1994 DNA computing deminance 37 - 1997 ASCI Red (Intel) deliv 38 - 1997 Linux cluster supercond 22 - 1991 Linux CS (Linus Torvor) 23 - 1990s -> Software for eminance 24 - 1990s -> Digital library te 25 - 1990s -> Machine learning -> Machine learning
12.00	basis for advances in cryptography, telecommunications, image processing, spoken-language technologies 3 - 1946 Monte Carlo computational estimation method (S Ulan, J von Neumann)	delivered to Census Bureau 9 - 1952 IBM 701 (Defense Calculator) 10 - 1952 MANIAC I built at LANL 11 - 1954 IBM 650 for built at LANL	 6 - 1960 COBOL "common business-oriented language" (DoD). G Hopper is primary developer 7 - 1960s -> Artificial intelligence R&D (spurs cognitive science, robatics, natural-language processing, adaptive and intelligent systems, human-machine communication, scientific visualization) 2 Security and the processing of the latter of the latter	11 1070 Palatianal database consent (E Codd IRM)	 1985 NSF university supercomputing centers 1985 First distributed-memory parallel platform (Intel). Developed for ORNL 1988 Cray Y-MP installed at NASA, LANL 	25 - 1990s -> Machine learnin 26 - 1993 Mosait Web browser 27 - 1994 Java language (Bill. 28 - 1994 Netscape (Mosait dev 29 - 1995 -> Globus software f 30 - 1996 Google search engine
Sector Sector	Labs) - enables compart, solid-state computer circuitry to replace huge arrays of vacuum tubes	12 - 1956 TX-O, first transistor-based computer (MIT) 13 - 1956 LARC (Sperry-Rand) for atomic research	8 - 1963 Sketchpad graphics system (1 Sutherland, M11) 9 - mid-60s NASTRAN structural design software 10 - 1969 Unix OS (D Ritchie, K Thompson, Bell Labs)	 12 - 1970 Ketanona duadase concept (C.Cody, Ibin) 12 - 1970s -> Computational complexity R&D (machine states, algorithms for structured programming, formal verification, crypotography) 13 - 1970s -> Spoken-language R&D 14 - 1970s Visualization innovations; WYSIWYG (C Simonyi) 15 - 1972 C language (D Ritchie, Bell Lobs) 14 - 10² D. Datations colditional charge relations 	 18 - 1883 GNU (for GNU's Not Unix) project (R Stallman, MIT) promotes "open-source," freely shared software 1985 Microsoft Windows 1.0 20 - mid-80s - > Software engineering metrics R&D 21 - late-80s -> Advanced discovery, data mining R&D 	18 - early 90s Processor in men
2 - lote 40s Čore m NETWORKS	2 - late 40s Core memory (J Forrester, MIT)	 4 - 1951 A-O compiler translates machine language into higher-order code (Grace M. Hopper) 5 - 1958 Formula Translation (Fortran), first high-level programming language (John Backus, IBM) 	 6 - 1963 Complementary metal axide semiconductor (CMOS) (Frank Wanless, Fairchild) 7 - 1964 Mouse, graphical user interface (GUI) (D Engelbart, Stanford) 8 - 1964 -> Moore's Law (G Moore, Fairchild) 	16 - mid-70s Prototype relational databases 17 - 1976-78 Public-key cryptography techniques 12 - 1971 Intel 4004, first single-chip CPU 13 - 1975 - > B&D in Very Longe-Scale Integrated (VLSI)	21 - late-80s -> Advanced discovery, data mining R&D 14 - 1980 Seagate ST-506, first 5.25" disk drive 15 - early 80s Redundant Arrays of Inexpensive Disks (RAID) for high-volume data storage (UC-Berkeley)	increasing supercomputing : 19 - 1990s Field Programmabl technology enabling system 20 - late 90s -> Quantum supe hybrid, and nano- compone next-generation high-end p
		 3 - 1954 Microwave Amplification by Stimulated Emission of Radiation (MASER) (C Townes, Columbia) 4 - 1956 Magnetic hard disk technology (IBM) 5 - 1958 Integrated circuit (J Kilby, Texas Instruments, and R 	 9 1966 Dynamic Random Access Memory (DRAM) 10 - 1967 Floppy disk, read-write drive (A Shugart, IBM) 11 - 1967 Head-mounted display, precursor of virtual reality (VR) technologies (I Sutherland, Harvard) 	13 - 1975 -> R&D in Very Large-Scale Integrated (VLSI) circuits - new chip-design methods and system architectures, such as Reduced Instruction Set (RISC) pro- cessing, enabling first "workstations," rapid chip proto- typing and fabrication	 16 - 1984 CD-ROM (Phillips and Sony) 17 - 1987 -> SEMATECH partnership for U.S. chip-technology leadership (Government and IT industry) 	12 - early 90s -> Optical switch
		Noyce with G Moore, Fairchild Semiconductor) 1 - 1957 -> Semi-Automatic Ground Environment (SAGE). First large-scale IT communications network. Whirlwind platforms linked to remote radars in North American Air Defense System. Innovations: modems, digital phone-line transmission, system duplexing, software for real-time operations; cathode-ray tube (CRT) screen	2 - 1960 Packet-switching principle (P Baran, Rand) 3 - 1967 Concept of decentralized computer network 4 - 1969 DoD commissions ARPAnet for research	5 - 1972 Ethernet (R Metcalle, Xerox PARC) 6 - 1973 Transmission Control Protocol (TCP) and Internet Protocol (IP) (V Cerf, Stanford, R Kahn) 7 - 1975 MFEnet, HEPnet (DOE) and NSFnet launched model internet sattware model internet sattware into an internet sattware internet internet	 8 - 1983 U.S. networks adopt TCP/IP standard 9 - 1986 NSF takes over ARPAnet; networks link in Internet 10 - 1986 Internet Domain Name System (DNS, such as .com, .orgedu) developed (P Mackapetris, USC) 11 - 1989 World Wide Web (T Berners-Lee, [CERN]); concepts include URL, HTML, and HTTP 	 14 - 1992 First multicast backbe 15 - 1994 -> IPv6 design for bi 16 - mid-90's Grid computing c 17 - 1995 LDAP network direct 18 - 1999 First end-to-end ell-o-speeds above 1 gigibit p/s (19 - 1999 -> Hardware and sol time, multimedia collaborat
Color Code: Text beneath upper color bars represe The 'impaat' arrows and the numbere are color coded to the column text, yel and development, white boxes and pr IMPACT	ents the timeline for that specific category. ed boxes below the impact timeline llow text is Federally funded research rows are funded by private sector.	959 Fres 24-boor to readst 1952 Univer predicts alection 1953 1952 Univer predicts alection 1958 Fres	ad anonic controlled missile in make to computer controlled missile in make to computer controlled missile in make to be first predicted in the second in the tob first predicted in the second in the second in the tob first predicted in the second in the second in the tob first predicted in the second in the second in the second in the tob first predicted in the second in the tob first predicted in the second in	Notes and anon-strates to space and and strateging of a space of the strates of the space of the	10001 metrend arcan 1980 metrend	antitest tenends run 1993 seconds antenits ystems 1993 runer and antenits ystems 1993 runer and 1993 compare 1999 runer and 1993 compare
COMPUTERS	2 3 4	5 6 9 11 12 7 10 13	14 15 17 16	18 19 21 23 20 22	24 26 29 30 33 25 27 31 33 28 32 32	34 35 36
SOFTWARE	1 <u>3</u> 2	4	6 8 9 10 7	11 15 16 17 12 13 14	18 19 21 20	22 26 27 23 24 25 28
COMPONENTS	1 2	3 4 5	6 7 9 <u>10</u> 8 11		14 16 17	18
NETWORKS		ī	2 3 4	5 6 9 7	8 9 11 10	12 14 15 13
Rise of Information Technologie	ss: Computing Machines WWII needs: ballistics, cryptography, flight simulation, nuclear physics		Operating Systems Multiple operations with less human involvement	Connected Systems Multiple users with terminals share time on system	Powerful systems Commodity Lo	works of Computers ocal Areo Networks (LAN) lide Areo Networks (WAN) Internet

THE EMERGING AGE OF **INFORMATION TECHNOLOGY** SCOPE OF FEDERAL R&D IMPACTS

ter

39 - 2000 ASCI White (IBM SP Power3) at LLNL achieves 7.22

teraflops 40 - 2001 NSF Lemieux (Compaq) at Pittsburgh Supercomputing Center, fastest system for U.S. academic research, attains 6 teraflops 41 - 2002 -> NSF Distributed Terascale Facility initiative develops world's first multi-site terascale system

- 2002 -> Middleware - software between applications OS that enables distributed computing and systems of

21 - 2001 - > Revolutionary concepts for system architectures to increase speeds, portability, and scalability of supercomputing platforms

20 - 2001 - > R&D in next-generation optical technologies, security, privacy, survivability, hybrid and wireless networking

ness co

ciples for high-quality softw

2000 -> Next-generation high-end systems and applications software for national priority missions
 2001 -> Software security, reliability, robustness,

2 -> Middley

systems

:90 hits 1 trillion flops r (D Becker, T Sterling,) onstrated (Adelman, USC) iter (Linux NetworX) to BNL

rvalds, Finnish student) mbedded systems ns R&E lov. Sun) ers) free software

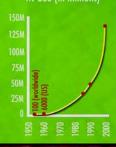
Page, Brin, St

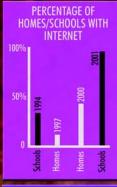
rray (FPGA)

37 38 39 40 41 29 30 33 31 32 20 21 18 19 16 17 20



NUMBER OF COMPUTERS IN USE (in millions)





DIGITAL INFORMATION UNITS

Bit A binary digit (0 or 1)						
Byte	8 bits					
Kilobyte	1000 bytes					
Megabyte	1,000,000 bytes					
Gigabyte	1,000,000,000					
	bytes					
Terabyte	1,000,000,000,					
	000 bytes					
Petabyte	1,000,000,000,					
	000,000 bytes					
Exabyte	1,000,000,000,					
	000,000,000					
	bytes					
Zettabyte	1,000,000,000,					
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Yottabyte	1,000,000,000,					
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	000,000 bytes					



Wireless Networking

Networking extended to wireless mobile, embedded devices and systems

Convergence of Technologies

Systems of systems, digital society



THE EMERGING AGE OF INFORMATION TECHNOLOGY: SCOPE OF FEDERAL R&D IMPACTS

The graphic timeline at left (please fold out front cover) provides an overview of the role of Federally funded research in the history of information technologies in the United States. The timeline's aim is to show the developing outlines of the digital revolution and some societal indicators of technological transformation. It is not intended to be comprehensive.

Reading the timeline

Four information technology areas are highlighted by color (computers, software, components, and networks). Numbered descriptions of developments in each IT area, by decade, run across the top, with Federally funded activities in yellow. Below, corresponding timelines for each area show the sequence of these developments. Arrows indicate impact milestones.

Selected sources: Federal agencies and laboratories; IT industry Web sites; *Funding a Revolution*, National Research Council, National Academy Press, Washington, D.C., 1999; Computer History Museum; Greatest Engineering Feats of the 20th Century, National Academy of Engineering; History of Computing Project; IEEE History Center; History of Computing, J.A.N. Lee, former editor, IEEE Annals of the History of Computing; History in the Computing Curriculum, Association for Computing Machinery (ACM); Chronology of Personal Computers, Ken Polsson; Common Gateway Interface (CGI) Historical Timeline, Wayne E. Carlson.

Disclaimer: Dates given for key developments often vary among IT histories. The variations typically do not change the broad direction of advances. Nonetheless, the editors regret any inadvertent errors in this timeline.



Networking and Information Technology Research and Development Advanced Foundations for American Innovation

Supplement to the President's FY 2004 Budget

A Report by the Interagency Working Group on Information Technology Research and Development

> Committee on Technology National Science and Technology Council

> > September 2003