

**Eco Forum II: Power in the Data Center: Measuring Server Efficiency**  
**March 27, 2006**

**AGENDA**

<b>Time</b>	<b>Topic</b>
8:00 – 9:00 a.m.	<b>Registration and Continental Breakfast</b> <i>Sponsored by Intel</i>
9:00 – 9:15 a.m.	<b>Welcome</b> Andrew Fanara, Team Lead, Energy Star Products, EPA
9:15 – 9:30 a.m.	<b>Outline of Meeting Goals</b> Jonathan Koomey, Lawrence Berkeley National Laboratory and Stanford University
9:30 – 11:00 a.m.	<b>Speakers:</b> <i>Mechanics of SPEC Benchmarking</i> Walter Bays, President SPEC  <i>Lessons Learned in Setting Internal Performance Metrics</i> Rick Hetherington, Distinguished Engineer Sun Microsystems  <i>Introduction to the Transaction Processing Performance Council (TPC)</i> Robert M. Lane, Senior Staff Engineer
11:00 – 11:15 a.m.	<b>Break</b> <i>Sponsored by Intel</i>
11:15 – 12:00 p.m.	<b>Review</b> List and categorize key questions to determine working groups
12:00 – 12:45 p.m.	<b>Lunch</b> <i>Sponsored by AMD</i>  <b>Speaker</b> Bruce Shaw, Director, Global Enterprise Marketing & Business Development, AMD
12:45 – 2:00 p.m.	<b>Working Groups</b> Focus on 4-5 key areas related to measurement conditions
2:00 – 3:00 p.m.	<b>Reporting</b> Groups report out
3:00 – 3:15 p.m.	<b>Break</b>
3:15 – 4:00 p.m.	<b>Open Discussion</b> Components of draft measurement protocol
4:00 – 4:30 p.m.	<b>Review Committee Selection</b>
4:30 – 5:00 p.m.	<b>Establish Next Steps</b> Conclude
5:00 – 6:00 p.m.	<b>Cocktail Reception</b> <i>Sponsored by AMD</i>