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Executive Summary

The Large Hadron Collider at CERN is due to begin operation at the end of 2007 opening a new energy frontier in particle physics. The LHC is ultimately expected to operate at $\mathcal{L} = 10^{34}\,\mathrm{cm^{-2}\,s^{-1}}$ and the CMS detector has been designed to cope with the high radiation and event rates expected at this luminosity. However, after several years of running at this luminosity, there will be strong motivation to increase the performance of the machine in order to expand the physics potential of the LHC. In addition, key elements of the detector and machine will not survive more than a few years at full LHC intensity.

An upgrade of the machine to $\mathcal{L} = 10^{35}\,\mathrm{cm^{-2}\,s^{-1}}$ the SLHC, is currently under study. The potential of a factor 10 increase in luminosity offers an opportunity to explore new physics, but will make severe demands on the CMS detector. It is likely that the tracking system will need to be replaced to cope with both the higher occupancy and radiation resulting from high luminosity running. In addition, the trigger system will have to be replaced, and the Level-1 may have to incorporate elements of the tracking information.

Design of detectors for CMS began in 1992, and it is clear from the experience of building the elements of CMS, that a minimum of 6 years would be required from first R&D on a new detector until deployment in CMS. Hence it is timely now at the start of LHC operation to begin development on the detectors required for the SLHC upgrade.

This Expression of Interest briefly describes the motivation and scope of the likely CMS upgrades for the SLHC. It is intended to outline the required avenues of R&D which must proceed in the next years in order to prepare for the SLHC, and will be followed by a more detailed Letter of Intent.

Structure of the Expression of Interest

Chapter 1, the Introduction, discusses the motivations for the upgrade.

Chapter 2 describes potential upgrades to the tracking system, and key areas of R&D which are required.

Chapter 3 motivates the replacement of the trigger system and gives indications on possible upgrades.

Chapters 4 and 5 detail the work which may be required to operate the calorimeter and muon systems at the SLHC.

Chapter 6 outlines the organization of the upgrade work, and gives indicative cost estimates and timescales for the upgrades.

Contents

Chapter 1

Introduction

The Large Hadron Collider (LHC) [1], at the CERN Laboratory, the European Laboratory for Particle Physics, outside Geneva, Switzerland, will be completed in 2007. The LHC will collide two proton beams, circulating in opposite directions, at an energy of 7 TeV each (centre-of-mass energy $\sqrt{s} = 14$ TeV). The accelerator has been designed to run at a peak luminosity of $\mathcal{L} = 10^{34}$ cm^{-2} s^{-1} .

The CMS experiment [2, 3] is a general purpose detector designed to explore physics at the TeV energy scale [4–6]. It is expected that the data produced at the LHC will reveal the electroweak symmetry breaking mechanism (EWSB) and provide evidence of physics beyond the standard model.

The detector and the accelerator were designed for a physics program which would deliver several hundred fb^{-1} of integrated luminosity at a peak luminosity of $\mathcal{L} = 10^{34}$ cm^{-2} s^{-1} Continued running of the LHC much beyond 500 fb^{-1} without upgrades of the machine and detectors will not be profitable due to;

- elements of the machine reaching their radiation damage lifetime,
- radiation damage to the central tracker,
- the very long time required to further accumulate a sufficiently large data sample to significantly increase the desired statistical precision.

An upgrade of the LHC, the *Super* LHC (SLHC), would aim to increase the peak luminosity by an order of magnitude, and to deliver ≈ 3000 fb^{-1} during its operation. The SLHC would require substantial changes to the machine near the interaction region of CMS, and also likely changes to the bunch structure of the machine. CMS would require replacement of the central tracker to cope with the higher occupancy and radiation as well as the trigger and data acquisition systems in order to cope with an increased data volume. It is expected that the calorimeter and muon systems will require only minor modifications, except possibly the very forward calorimeters.

1.1 The SLHC

The LHC machine was designed for a peak luminosity of $\mathcal{L} = 10^{34}$ cm^{-2} s^{-1} It is likely that it will take several years of operation of the LHC before this level can be achieved.

By increasing the the number of protons in each bunch from 1.15×10^{11} to 1.7×10^{11}, and upgrading the LHC RF system it may be possible to reach $\mathcal{L} = 2 \times 10^{34}$ cm^{-2} s^{-1} after 4-5 years of running. This would be the *ultimate* luminosity [7] achievable with the current

Machine elements. Figure 1.1 shows a potential scenario for the delivered luminosity of the LHC machine for the case of design luminosity being achieved after 3 years of operation, and a second case of exceeding design luminosity by a factor of 2 after 5 years operation. In both cases the lifetimes of the tracking detectors are exceeded after around 7 years. The quadrapole triplets near the interaction region have a lifetime of around $700\,\mathrm{fb}^{-1}$, which would likely be reached in about 10 years of operation. Also shown are the number of years required to halve statistical errors as a function of years of operation. It rapidly takes a very long time to substantially reduce statistical errors when the peak luminosity remains constant.
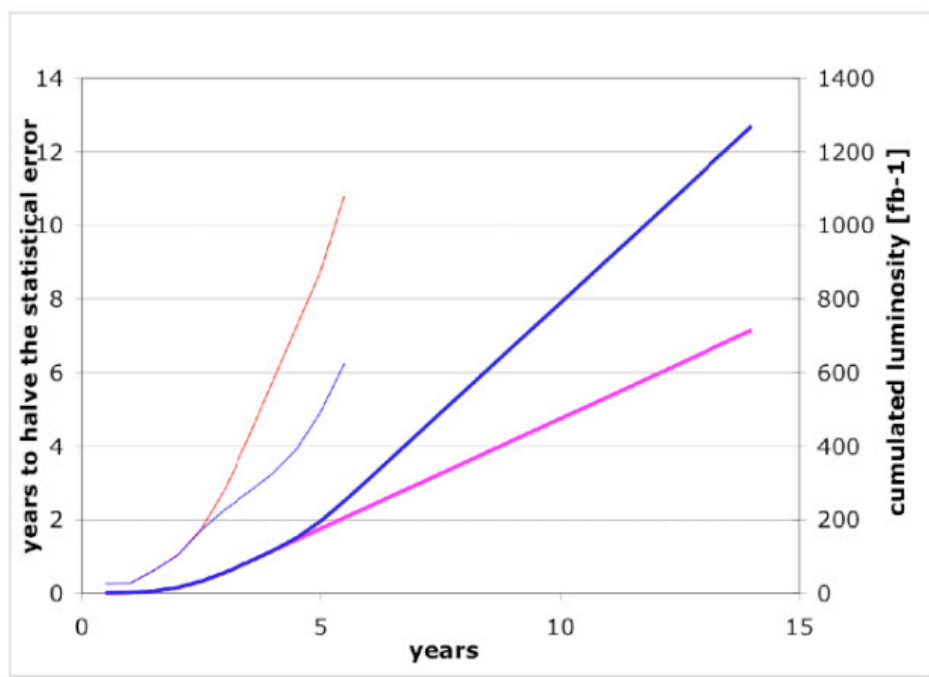
Figure 1.1: The thick lines on the right show integrated delivered luminosity (right hand scale) for two potential LHC running scenarios as a function of years from startup. The thin lines on the left (left hand scale) show the run-time required to halve statistical errors. [7].

In order to reach a factor of 10 increase in peak luminosity, $\mathcal{L} = 10^{35}\,\mathrm{cm}^{-2}\,\mathrm{s}^{-1}$, it is proposed [7–9] to replace the machine elements around the interaction region as well as changing the bunch structure. Two proposals are currently being considered. In the first, the number of bunches are doubled, and the bunch crossings arrive at 80 MHz (12.5 ns between crossings) instead of the 40 MHz at the LHC. In this scenariao, the number of minimum bias pile-up events in each crossing increases over the LHC nominal by a factor of ≈ 5. Unfortunately this configuration increases the susceptibility to electron cloud effects in the machine, and it may be difficult to achieve full peak luminosity. The second proposal would have slightly longer bunches with a greater number of protons arriving each 50 ns. This would ease the electron cloud effects, but would increase the number of pile-up events in each crossing to more than 300.

1.2 The CMS detector

The CMS detector measures roughly 22 meters in length, 15 meters in diameter, and 12,500 metric tons in weight. Its central feature is a 4 Tesla solenoid, 13 meters in length, and 6

meters in diameter. Along with the central silicon microstrip tracking detector, the electromagnetic and hadronic calorimeters are contained within the solenoid coil. Muon detection is embedded in the flux return iron of the magnet. A drawing of the detector can be seen in Figure 1.2, and a detailed description of the construction and performance of its detector systems can be found in [10].

Figure 1.2: The CMS detector

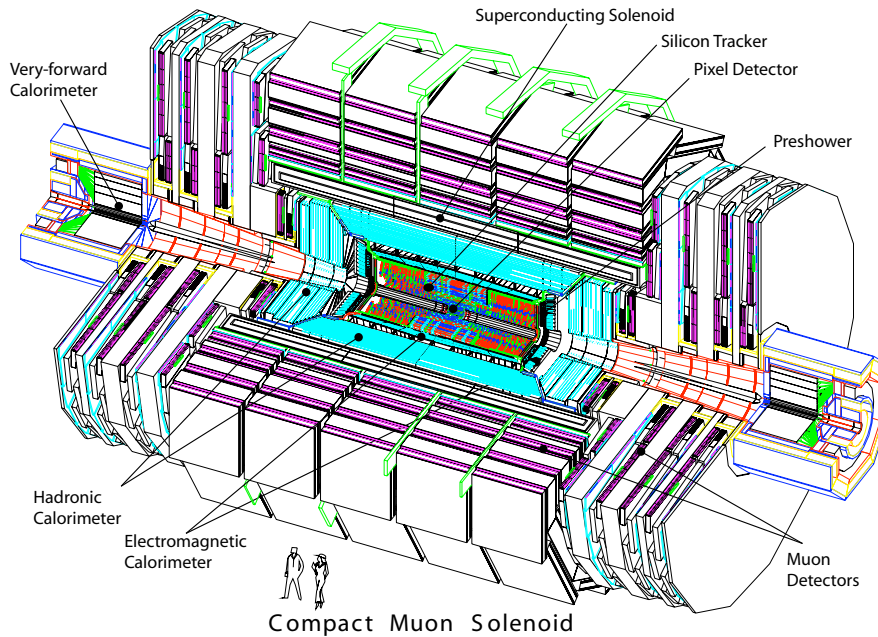The innermost tracking for the barrel region (BPIX) is accomplished with a three layer (4.4 cm, 7.3 cm, 10.2 cm) silicon micro pixel detector and with two disks of pixels (FPIX) in each forward region (34.5 cm and 46.5 cm from the IP) with a total area of approximately $1\,\mathrm{m}^2$ composed of 66 million $100 \times 150\mu\mathrm{m}^2$ area pixels. These detectors are designed to withstand the high occupancy and fluence of the LHC. The remaining tracking layers are composed of 11 million channels of silicon microstrip detectors. In total nearly $200\,\mathrm{m}^2$ of detectors organized in an inner barrel (TIB) 4 layers occupying a radius 20 cm – 50 cm, an outer barrel (TOB) 6 layers occupying a radius 55 cm – 120 cm, and two endcap detectors (TEC,TID). At nominal luminosity, the expected occupancy of the strips in the TIB at LHC is 2–3% while the occupancy in the TOB should be ≈1%. As described above, the SLHC machine upgrade scenarios could increase these expected occupancies by up to a factor of 20. A combination of increasing the area of the pixel detector, and shortening the strip length will be required to cope with the increased occupancy. In addition new technologies may be required for the innermost layer where the radiation effects will be extreme.

The electromagnetic calorimeter has a barrel section covering the region $|\eta| < 1.479$ and two endcaps completing the coverage up to $|\eta| < 3.0$. The barrel consists of 61200 lead tungstate crystals read out by silicon avalanche photodiodes (APDs). The two endcaps contain a total of 14648 crystals read out by vacuum phototriodes (VPTs). Hadronic calorimetry is achieved with scintillator embedded in brass absorber, where the light is read out using hybrid photodiodes (HPDs). The barrel sections of these calorimeters are expected to operate even in the extreme environment of the LHC, although the forward regions may require replacement or modification. The fate of the very forward Hadronic calorimeter $3 < |\eta| < 5$ may de-

pend on modifications required in the CMS interaction region in order to achieve the SLHC luminosity, and the importance for physics of the very forward region at high luminosity.

The CMS muon detection system uses three technologies. Drift tubes (DT) are used in the CMS barrel, Cathode Strip Chambers (CSC) are used in the endcaps, and Resistive Plate Chambers (RPC) are used in parallel with the other detectors in both the barrel and endcap. The muon system is well shielded by the CMS iron yoke, and it is expected that the detectors should continue to operate in the SLHC regime, with only a potential need for changes in the shielding in the forward regions ($2 < |\eta| < 4$) and possible upgrades for the on-detector electronics required.

The CMS trigger takes input from the calorimeters and muon systems to form a Level-1 decision. At each Level-1 trigger, the data is sent to a higher level software trigger (Level-3) processing farm where the full event data is available for making a trigger decision. The current maximum rate for Level-1 triggers is 100 kHz. It is proposed that any upgrade continue to respect that limit, but this implies a required increase in the bandwidth of the data acquisition system. In addition it appears that the current muon and calorimeter triggers may not have sufficient capability to reject background at high luminosity. In order to greatly increase the rejection power of the Level-1 trigger, it is proposed to bring in information from the upgraded tracking detector. This requires substantial new developments in the tracking system, and a complete replacement of the central trigger, but offers the possibility of much more information in the trigger decision taken at the SLHC

1.3 Physics Case

The SLHC, with its order of magnitude greater luminosity, will extend the discovery reach of the LHC for new particles such as those arising from Supersymmetry, or new forces and will allow for detailed measurements of Standard Model processes and any new phenomena discovered during LHC operations. While it is difficult to predict what physics will be most important to study after the LHC era, some specific possibilities are listed here that can benefit from the increased luminosity of the SLHC. Not addressed here, but equally important, are upgrades to the detectors and electronics that can improve upon the physics measurements even without increased luminosity (such as by lowering E_T thresholds, adding additional detectors, bringing in tracking into the Level-1 trigger, or by reducing experimental systematic uncertainties in some manner).

1.3.1 Standard Model

The precision measurement of electroweak parameters is a tool to look indirectly for physics beyond the Standard Model. One such measurement is that of multiple gauge boson production. The luminosity of the SLHC will provide for improvements on the limits of anomalous triple and quartic gauge boson couplings by a factor of about two, depending on the coupling. For this measurement, only the leptonic decay channels of the bosons are used, thus a good lepton identification and acceptance similar to the one for the LHC environment must be guaranteed. The sensitivity of the measurements will reach that of the radiative corrections in the Standard model (~ 0.001) for λ type couplings, and should therefore allow for a meaningful test of these corrections and others that arise for example in SUSY models. Even measurements of events with four gauge bosons in the final state will become accessible at the SLHC.

The Standard Model Higgs, if it exists, will have been discovered by the time the SLHC starts its operation. It will however remain important to measure its properties more precisely. For example the SLHC offers the opportunity to improve the measurement of the ratios of couplings with up to a factor two if both the $H \to \gamma\gamma$ and $H \to WW$ decay modes can be detected with similar accuracy as for the data collected at the LHC. This will allow the measurement of the the ratios of couplings to better than 10%. Rare Higgs decays can be observed, i.e. the important channel $H \to \mu\mu$ becomes accessible, and the Yukawa coupling can be determined to better than 20%. The most important and probably most challenging measurement will be the one that can map out the shape of the Higgs potential, and thus provide the ultimate proof of the Higgs mechanism as the one responsible for electroweak symmetry breaking. The information on the potential is extracted from the Higgs self-coupling, measured in events which contain two Higgs bosons in the final state. The cross section for this process is small, of the order of a few tens of femtobarns. The decay channel that can be used is $HH \to WWWW \to \nu ljj\nu ljj$. Hence good jet reconstruction in the experimental environement of the SLHC will be mandatory. With the luminosity of the SLHC, the self-coupling can be measured with a precision of 25% if the Higgs has a mass between 160 and $180\,\text{GeV/c}^2$, and will be sufficient to demonstrate that the minimum of the Higgs field is not at the origin. Finally, the discovery potential for heavy Higgses such as the H, A bosons in SUSY models can be extended by roughly $100\,\text{GeV/c}^2$ in the $\tan\beta - M_A$ plane.

If no Higgs is discovered then it is expected that the high energy scattering of electroweak gauge bosons will show structure beyond that expected in the Standard Model at WW and ZZ masses of order of 1 TeV. The discovery of such effects may be very difficult at the LHC. A factor of 10 increase in luminosity may be required in a number of scenarios, to discover e.g. ZZ scalar or vector resonance production. Again isolated high p_T leptons need to be measured with similar efficiency and quality as during the LHC running. The WW, ZZ scattering analyses require also jets to be tagged in the region of $2 < |\eta| < 5$, i.e. the so called forward jet tagging.

Most of the top quark studies at the LHC will have been done before SLHC comes into operation. An important exception is the search for rare top decays. In order to gain sensitivity with respect to the results obtainable at the LHC, it is however imperative that the ability to tag b-quarks with a secondary vertex technique is conserved at the SLHC. If so, a sensitivity to the decays $t \to qZ$ and $t \to q\gamma$ of order 10^{-6} is achievable, which is of interest for several theories of New Physics.

1.3.2 Beyond the Standard Model

1.3.2.1 Supersymmetry

If Supersymmetry (SUSY) has not yet been discovered in data samples collected during LHC running, inclusive searches may continue with the larger integrated luminosity of the SLHC. For example, in the minimal Supergravity model (mSUGRA), a search for an excess of events in the high-E_T jets and large E_T^{miss} signature can extend the discovery reach by an additional 0.5 TeV in squark and gluino mass (from 2.5 to $3.0\,\text{TeV/c}^2$) in going from $100\,\text{fb}^{-1}$ to $1000\,\text{fb}^{-1}$ [11]. Such a search is based on measurements made by the calorimeters, where the jet energy deposition (and thus the jet E_T threshold) scales with the SUSY mass scale. For inclusive searches involving leptons, we note that the lepton p_T is less correlated with the SUSY mass scale since many leptons originate from the cascade decays of SUSY particles. Thus, for searches involving leptons, it is important to maintain similar trigger and offline p_T thresh-

olds as during LHC operation.

For some of the scenarios studied in [12], where the sparticle masses are around $2\,\mathrm{TeV/c^2}$, the yield of sparticles at the LHC in usable decay channels is too low to detect anything more than a hint for a new particle. The SLHC will then make the discovery and allow for mass measurements. An example is the so called point K ($m_0 = 1000\,\mathrm{GeV/c^2}$ and $m_{1/2} = 1200\,\mathrm{GeV/c^2}$). The effective mass of the jets, leptons, and $E_\mathrm{T}^\mathrm{miss}$ will show a clear excess of 500 events over a background of 100 events for SLHC luminosities. Also the exclusive decay channel into $q\tilde{\chi}q\tilde{\chi}$ becomes observable at the SLHC with 120 signal events for 30 background events at an integrated luminosity of 3000 fb^{-1}. The decay $\tilde{\chi}_2 \to \tilde{\chi}_1 h$ becomes detectable with the same luminosity.

If evidence for SUSY is discovered, it will be important to measure more exclusive final states in order to elucidate the sparticle mass spectrum. This can be done by measuring dilepton mass edges, for example, which is a striking signature of mSUGRA models and whose value depends on the masses of the first and second-lightest neutralinos. But information can also be extracted from the edges and thresholds of quark-lepton, quark-lepton-lepton, and quark-quark-lepton-lepton distributions as well, which depend on combinations of the neutralino, squark, and gluino masses [5, 13, 14]. The study reported in [13] indicates the feasibility in carrying out a deconvolution of the sparticle masses for the low mass mSUGRA benchmark point "SPS1a," and to extract masses with an accuracy of a few percent using LHC luminosities. However, for higher mass SUSY scenarios, the statistics available for such measurements will be much reduced and SLHC luminosities may be required to disentangle the sparticle masses.

While to merely extend the mass reach search for evidence for SUSY to masses of about 3 TeV high E_T jet measurements will be especially important, in order to disentangle the particle spectrum it will be important to maintain the present tracker capabilities and performances in terms of of b and τ tagging and lepton isolation, thus the need for improved granularity of the tracker.

Equally important, but difficult, is the attempt to determine the spin of the new particles to understand whether counterparts to the Standard Model particles are observed with opposite spin statistics, or whether some other new phenomenon is observed such as Universal Extra Dimensions, which can emulate the mass spectrum of SUSY. For example, as reported in [15], it is possible to determine the spin of the $\tilde{\chi}_2^0$ in the decay $\tilde{q}_L \to q_L\tilde{\chi}_2^0 \to q_L\ell\tilde{\ell}_R \to q_L\ell^+\ell^-\tilde{\chi}_1^0$ by examining SUSY events containing two oppositely-charged same-flavour leptons and a high E_T quark, and comparing the difference in the invariant mass distribution of $m(\ell^+ q)$ versus $m(\ell^- q)$. The dependence of the asymmetry of the distribution with the lepton-quark mass is sensitive to the spins involved in the decay, and since the LHC is a pp collider it produces more squarks than anti-squarks so the effect is not washed out. The sign of the asymmetry also indicates which slepton chiral state is involved in the process. A feasibility study in [15] shows that the reconstructed distribution is distinguishable from the case of no spin correlations for one studied mSUGRA point ($m_0 = 100\,\mathrm{GeV}$, $m_{1/2} = 300\,\mathrm{GeV}$, $A_0 = 300$, $\mu > 0$, $\tan\beta = 2.1$) assuming at least 150 fb^{-1} of integrated luminosity. While more work will be needed to understand if this result is general enough to distinguish SUSY from UED in more than one instance, it is promising and clearly will require SLHC-sized data samples.

1.3.2.2 New Gauge Bosons

Many grand unified theories, extra dimension theories and superstring inspired theories predict additional heavy neutral and charged gauge bosons (Z', W'). Such bosons lead to striking resonances in the dilepton final state above the Drell-Yan spectrum. The SLHC reach can be extrapolated from that expected for the LHC since the sensitivity to pile-up is expected to be small. While the amount of integrated luminosity required for a 5σ discovery at a given Z' mass depends on the particular model (see for example Figs. 3.20 and 14.23 in [16]), in general one can expect to be able to extend the reach in Z' mass by about $1.2\,\text{TeV/c}^2$ in going from a $100\,\text{fb}^{-1}$ sample to a $1000\,\text{fb}^{-1}$ sample for both dielectron and dimuon final states (extrapolating from the studies reported in [16]). This implies a sensitivity to neutral gauge bosons with masses up to 5–6 TeV/c^2 depending on the considered models. For an asymptotic limit of $3000\,\text{fb}^{-1}$, one should expect a discovery reach of $6.5\,\text{TeV/c}^2$ for some Z' models. The reach is similar for W' bosons. [11].

1.3.2.3 Dilepton Resonance Spin Discrimination

If a resonance is observed in the dilepton final state, the next step would be distinguish between the possible Z' models and to differentiate between a spin-1 Z' resonance and a spin-2 graviton, appearing in various extra dimension models, through measurements of the forward-backward asymmetry and the angular distribution of the decay. The forward direction in a symmetric pp collider is taken to be the direction of motion of dilepton system, which becomes a better approximation of the true incident quark direction the higher the rapidity for the decay products. Thus, it is important to maintain good coverage in the forward region up to rapidities of $|\eta| > 2$, for identifying the leptons used in the angular measurements analysis.

The ability to distinguish between several Z' models via measurements of the forward-backward asymmetry is addressed in [16, 17]. The conclusion of the statistical analysis in [17] is that with an integrated luminosity of $400\,\text{fb}^{-1}$, one can distinguish between either a Z_χ or Z_{ALRM} and one of four other Z' models (Z_{SSM}, Z_ψ, Z_η, and Z_{LRM}) with a significance level of more than 3σ up to a Z' mass of between 2.0 and 2.7 TeV/c^2. One can distinguish among the four other Z' models with the same level of significance only up to a mass of 1–1.5 TeV/c^2, whereas Z_{ALRM} and Z_χ are indistinguishable for masses larger than $1\,\text{TeV/c}^2$. Clearly if a Z' resonance is discovered at a higher mass than these ranges, or if one is unlucky in the Z' couplings, the significantly larger integrated luminosities achievable at the SLHC will be required.

The discrimination between spin-1 and spin-2 resonances at the LHC is discussed in [18], and a feasibility study using the CMS detector is presented in [16]. In order to distinguish between a RS graviton from a Z' at the 2σ level at a mass of $3.0\,\text{TeV/c}^2$ requires $290\,\text{fb}^{-1}$ for a coupling of $c = 0.1$, increasing to $1200\,\text{fb}^{-1}$ for a coupling of $c = 0.05$. Discrimination between spin-1 or spin-2 from spin-0 requires even more integrated luminosity, as does distinguishing the spin of higher mass resonances.

1.3.2.4 Compositeness and other BSM signals

The tenfold increase in luminosity at the SLHC will give access to jets with $E_\text{T} \sim 4.5$ TeV. This offers the opportunity to extend the search for quark substructure. The sensitivity can be increased by a factor 1.5.

An increase in mass reach of about 30% when increasing the integrated luminosity from 300 fb^{-1} to 3000 fb^{-1}, is expected for heavy mass objects searches in the TeV range, as predicted various other theories.

Chapter 2

Tracker Upgrades for SLHC

2.1   Introduction

The main motivation for a luminosity upgrade is to provide more statistics to improve physics studies beyond those possible at the LHC. Detector performance should remain similar to that at the LHC in the presence of higher particle fluxes, detector occupancies, trigger rates, and radiation damage. The inner tracking detectors will need complete replacement, with new systems adapted to the even harsher SLHC environment.

Until the LHC has been operating for some years and discoveries have been made, the principal future physics areas will remain speculative. In the examples of SLHC physics discussed in Chapter 1.3, electron and muon track reconstruction will remain as important but there will be greater numbers of energetic jets with more particles and higher track densities. Higher granularity of the tracking system will evidently be required, which poses important questions of channel count, power and material budget.

The magnitude of tracker R&D and construction will be guided by LHC discoveries since most require several years of operation and data analysis. Issues arising from LHC machine operation might be visible early and guide the design. However, the design of a new tracker, with outline plans for power, cooling and other services needs significant time. Therefore the strategy proposed by CMS is an intermediate step before final commitments are made to a replacement tracker, if possible to install a new pixel layer about 5 years after LHC start-up. This would provide a means of demonstrating new technical solutions at reasonable cost before embarking on the complete tracker replacement and allow some experience to be gained, or tuning of the detector design.

In addition to upgrades of the CMS trigger off-detector electronics, it has emerged that it is highly desirable to include tracker data for the first time in the level-1 trigger. The principal reason is that transverse momentum thresholds will no longer provide sufficient discrimination at the SLHC to control trigger rates. However, implementing a tracking trigger represents a major challenge.

2.2   Conclusions from CMS SLHC workshops

CMS has organised four SLHC workshops since 2004, the latest in April 2006 [19], to prepare for experiment upgrades. One conclusion which has not so far been challenged is that there is no obvious reason for major changes in specifications for momentum and spatial resolution, which determine detector parameters. Tracker sensor lengths should reduce to cope with in-

creased occupancy, although it should be noted that CMS can probably operate with higher occupancy values than at present. Studies of track reconstruction, using zero-suppressed data, in heavy-ion collisions [20] show very promising performance, with occupancies comparable to SLHC, but with lower LHC clock speed. Operating with increased occupancy would certainly help to reduce power requirements and channel count. However it should be noted that some aspects of heavy ion operation are very different from that with protons, in particular proton-proton collisions in LHC give rise to many collisions in the same beam crossing. At the SLHC, there will be an even higher number and this will add complexity to the track reconstruction and pattern recognistion.

Although the centre-of-mass energy of the SLHC will be the same as that of the LHC, emphasis will shift to exploring rarer events with more energetic jets. Tracks in such jets tend to be very close to each other, thereby complicating pattern recognition. It remains to be studied what implications this has for increased tracker granularity.

Several issues have been highlighted in these discussions.

- Power will be a crucial issue, despite trends in lower power circuits and lower supply voltages in deep sub-micron CMOS. There are several reasons including channel count, amplifier noise issues, power dissipation by silicon sensors and power dissipation in cables.

- The material budget should not increase. Improvements in physics performance would arise from material reductions and likely to be more beneficial than over-specifying granularity which will cost power and material.

- The costs of present pixel systems are estimated to be $500 \, \mathrm{CHF/cm^2}$, dominated by bump-bonding, but, even if significant reductions can be made, will remain considerably higher than current microstrip system costs ($40 \, \mathrm{CHF/cm^2}$).

- Despite the experience gained in the construction of the present tracker, it should be anticipated that large systems will remain challenging to build, and the time needed for both R&D and system qualification is usually underestimated. CMS was successful in pioneering automation of tracker module production but the large number of module variants adds considerably to logistics problems. If a reduction in the number of module types could be achieved, assembly and integration tasks would be simplified. Further increase in automation of assembly, possibly even including outsourcing, should be considered.

- Sensor radiation hardness is a concern, but principally for innermost layers, and the choice of pixel sensor material is an open question. However, the optimal choice of sensor material for outer layers may be different than at present.

- Off-detector electronics will benefit significantly from technology evolution, as well as computer processing power, which should be borne in mind when specifying the occupancy requirements. However, following some of the technology trends will remain challenging, given the special constraints imposed by tracker construction, especially for customised low-mass components and optical links.

- The trigger requirement is considered by many to be vital to the upgrade for SLHC. More selective, and potentially biased, trigger selection criteria will be required to contain the rate. The proposed SLHC latency of $6.4 \, \mu\mathrm{s}$, i.e. 512 bunch crossings at 80MHz, will need deeper on-chip buffers than at present.

2.3 Pixels and Strips for the SLHC

The present CMS tracking is based on the massive use of silicon strip detectors for the outer tracking regions and on hybrid pixel technology to deal with high particle track rates in the vicinity of the interaction region. Although both systems use fine pitch segmented silicon as a basic sensor material there are a few fundamental differences that distinguish the two systems.

The silicon strip systems (TIB, TOB, TEC) are based on a non-zero suppressed analog readout. The strips with their long aspect ratio use fine pitch wire bonding to perform the connection between the sensor elements and the readout chips. In addition the strip system is characterized by the fact that the sensor surface is much larger than the silicon surface of the readout chips.

The pixel systems (BPIX, FPIX), however, are based on a completely different zero-suppressed readout scheme. In addition, a 2-dimensional connection technique (micro-bump-bonding) is used to bring the sensor signals to the readout chips and therefore requires a one to one coverage of the sensor surface with readout chips.

However, the crucial difference between pixels and strips may well be the way they achieve position resolution in both directions. In case of pixels this is done by keeping the sensor element (pixel) very small in both directions, whereas for the strips a stereo-technique is needed to get sufficient coordinate precision in the long direction of the strips.

In a future SLHC tracker the distinction between strips and pixels may well become much more blurred, since zero-suppression and bump bonding could in principle also be used in outer strip-like layers. The way position resolution is achieved in both directions, however, might become the crucial criterion to distinguish between pixels and strip-like sensors.

In the present CMS tracker the radial division between the strip system for medium and large radii (> 20 cm) and the hybrid pixel system for small radii (< 12 cm) is very much the result of a compromise between cluster merging and channel occupancy and exploding costs in the case of the pixel system. In addition it should be mentioned that the power density of the pixel system is about ten times higher than for the current strip system.

As the luminosity increases and we go from the LHC machine to the SLHC, the border between the two systems will clearly have to move outward. Although the present pixel technology is perfectly able to deal with the SLHC track rates and densities in the medium radius range it is also clear that costs of the current hybrid pixel technology need to be brought down substantially. Either this is done through a low cost technology or new, different pixel technologies (like MAPS, monolithic pixels, amplifying pixel structures etc.) have to be developed which avoid the use of the currently expensive micro-bump-bonding.

The conception and design of a new SLHC tracking system will certainly need a large number of careful studies to be done with some following key issues to be addressed:

- Channel density for the different layer radii. This is probably the most sensitive parameter defining the power dissipation of the system. A small pixel/strip size implies a small sensor capacity, which in turn gives a good signal to noise performance with a given preamplifier power. However, the increase of channels per unit area may still result in an overall higher power consumption. This is due to a minimal power level that is always required for every readout channel and which does not scale with the sensor capacity. This problem may even get worse due to

the fact that smaller pixels will create larger cluster sizes, which in turn will result in increased data traffic. This again costs more power and requires more data links (material budget) to the outside world.

- Powering schemes to reduce material budget. The present pixel system has on-chip regulators that allow smaller LV cable cross-sections within tracking volumes. Other possibilities to be studied would be serial powering schemes or DC-DC converters (charge pumps). Issues of failure tolerance, load variations and system stability need to be studied.
- Development of new low mass cooling techniques, that allow robust operation even in the case of individual modules (heat loads) being switched off.
- Implementation of triggering layers in the tracking system. Any triggering functionality will always cost extra power and extra material budget. It has to be carefully considered what would be the optimal radius for this material while getting the best triggering benefits.
- Development of high bandwidth readout schemes with lowest possible power dissipation.
- Reduce material budget overhead due to plugs and connectors within the sensitive tracking volume.
- How to ensure easy assembly, integration and repair capability. Special emphasis should go on minimizing interdependencies off the different subsystems in planning, assembly and integration.
- Development of low cost single sided silicon sensors that after heavy irradiations allow operation in a partial depletion mode (n+ on p-sensors).
- Cost scaling for other different non-hybrid pixel technologies

This list is certainly not complete and many other issues will have to be studied as well. The decision on how many subsystems and how many different technologies are needed is far from trivial. It seems clear, however,a tracker for theSLHC should not only be able to deal with a *20 times higher occupancy* (factor ten from luminosity and a factor two from the recent LHC machine group recommendation to use a 50ñs bunch crossing scheme) and at the same time *improve on the material budget problem* which is already the biggest concern with the current silicon tracker.

The RD to resolve these issues must be accomplished relatively soon. The innermost pixel barrel layer, and perhaps the endcaps, will suffer radiation damage and will probably need to be replaced after about 4 years of LHC operation. This will occur a few years before the major shutdown for the SLHC upgrade. Fortunately, the pixel detector is mounted on rails so that it can easily be removed and reinserted into the detector during shutdowns. This feature makes it relatively easy to upgrade the detector. This region also has some unused space and some unused cable runs. Taken together, these properties provide the opportunity to try out parts of the SLHC technologies and detectors in the context of doing physics in the latter part of the first phase of LHC operations. An intermediate partial tracker replacement that is aimed at implementing some of the features needed for the SLHC, including new tracking layers designed for use in the Level 1 trigger, is often referred to as the "pathfinder"system.

On October 9–12, 2006, the CMS Pixel Group, together with members of the silicon strip tracker group and the trigger group, held a workshop to discuss these issues. Key recommendations for R&D were:

1. to adopt or develop a simulation program capable of quickly allowing one to explore alternative geometries to determine the best arrangement of tracking detectors as a function of radius;

2. to identify specific "benchmark" physics analyses that help to establish the physics case for a CMS tracking trigger. The goal is to use these physics analyses to evaluate the performance of different trigger algorithms and use the performance metrics to select a baseline trigger design;

3. to undertake research in technologies that show promise of addressing some of the challenges listed above. For readout chips, these include ASIC technologies with smaller feature size than the $0.25\,\mu$m CMOS used in the current detector. For sensors, they include silicon with special processing to improve radiation hardness and the new materials and approaches listed above. One caveat is that the chosen technology must be mature enough at the time of construction that it is certain that the detector can be built on time and to meet its performance requirements. Technologies that offer little hope of achieving this level of maturity in time for construction should not be pursued; and

4. to carry out research into new methods of providing mechanical support, power and cooling, and lower mass cables and to study the tradeoffs between ideal coverage and ease of assembly.

2.4 Sensor issues

Research into improved sensors continued after the experiments made commitments to their sensor technologies [21] and CMS collaborators have been active in R&D projects. Some of the results suggest that silicon can meet radiation hardness requirements for the outer tracker, but they could require sensor manufacturers to adopt less widely used substrate materials, such as p-type silicon, or more innovative materials, such as magnetic Czochralski silicon. The cost issues associated with this and any need for, e.g., double-sided processing must be clarified. For the inner layers of a new tracker, it is not clear if sufficient radiation tolerance can be provided by any material and a strategy, either to replace layers at regular intervals, or adopt a new radiation hard sensor material must be defined.

Narrowing down of sensor options, in close collaboration with major manufacturers, will be required at an early stage, since specification of front-end electronics needs knowledge of signal properties and requirements for new cooling systems must be defined. It seems likely that dynamic range constraints will preclude design of amplifiers capable of handling both polarity signals. Leakage currents will be a major contribution to noise, as well as power, and the extent to which they can be controlled by lower temperature operation, as well as potential thermal runaway requires clarification. Signal speed and substrate doping changes have been controlled in part by high bias voltage operation to date; whether it is necessary or feasible to use even higher bias voltages in future must be understood.

Experience from LHC R&D illustrates the importance of working closely with the few major manufacturers who will be able to deliver large volumes of reliable, qualified sensors. Thus far, most of the prototyping of new sensors and materials in R&D projects has been based on small scale devices which are not yet representative of either the issues which will be encountered in mass production nor those which might be related to important details of actual sensor designs.

At least for the outer tracker, incremental gains in sensor performance will continue to be important. Qualification at the system level will be vital, and sufficient time must be left to allow revisions following early prototyping. Therefore, any sensor innovations must probably be limited to those which can reach large scale maturity in only a few years.

2.5   Electronic issues

CMS demonstrated over the last few years that $0.25\,\mu$m CMOS technology provided an excellent solution for on-detector ASICs, and it has been shown to be sufficiently radiation hard for the SLHC. However, it is expected to be commercially obsolete when an SLHC upgrade is undertaken. In more advanced processes, which are already in use, feature sizes and operating voltages have decreased considerably.

Although a few alternatives to CMOS have been discussed, notably Si-Ge technology, there are strong arguments to maintain the commitment to commercial CMOS, most likely with $0.13\,\mu$m feature size:

- CMOS radiation tolerance appears well in excess of SLHC requirements,
- Both analogue and digital designs are possible in the same technology, with adequate speed,
- There is considerable experience in the HEP community of CMOS design and significant benefits from sharing knowledge,
- The cost of engineering runs in all advanced processes is considerably higher than $0.25\,\mu$m, although large scale fabrication is likely to have a comparable, or lower, cost than the past. It will be important to plan prototyping, and share costs.
- Alternative technologies will require equivalent expertise to be gained and their performance is still to be demonstrated, which is likely to be costly and time consuming.

Use of smaller feature sizes will allow more functions to be incorporated into front-end chips, including deeper buffers, and lower power designs can be developed. However, there will be new challenges such as increased sensitivity to Single Event Upsets and reduced dynamic range of low voltage circuits. Operation at lower temperatures also requires increased attention to circuit performance. The present readout system provides analogue data, which proved to be immensely beneficial during prototyping and qualification, as well as offering benefits in position resolution when CMS operates. However, commercial electronic technologies are mainly digital, and the requirement for analogue data should be reconsidered and, if necessary an implementation matched to system needs, especially data transmission, should be implemented.

It is natural to choose a clock speed for digital electronics identical to the LHC beam crossing frequency which now seems likely to decrease from 40MHz to 20MHz, although this is not yet finally decided. oIt is crucial for development of a new readout system that the clock frequency is well defined at an early stage.

For off-detector electronics, the trend towards increased use of FPGAs is expected to continue. This will permit further development of flexible and more powerful data processing, which will be beneficial in handling the large tracker data volumes expected at the SLHC. However, constraints on underground rack space and heat removal probably imply that in-

creases in channel count should be accompanied by higher density boards, whose cooling will need careful engineering. Each board will probably need multiple outputs to deliver data to the DAQ switch, which will place additional pressure on board density. Irrespective of the detailed design of the tracker, data from each SLHC bunch crossing will contain more hits than at the LHC and most of them will be required for off-line track reconstruction. Thus the volume of tracker data to be transferred each second will increase substantially. Although the bandwidth of the switch matrix to the computer farm was a limiting factor in the original CMS DAQ design, such technology has already evolved considerably and commercial developments driven by consumer requirements are expected to meet CMS needs in future.

Two important items which must underpin development of a new tracker are optical links, and Timing, Trigger and Control distribution system, both of which are expected to be different for the SLHC. The $1.3\,\mu$m edge-emitting single-mode laser technology pioneered by the CMS tracker for data transmission was adopted throughout the experiment, including for Gb/s links. It was used both for data transmission and control signals. Although it proved a good choice, optical technology has since evolved further, and cost and performance considerations motivate a review of a future system.

The TTC system requires upgrading to the SLHC clock speed, and must provide precise clocks for multi-Gb/s. It would be wise to build a system compatible with the optical distribution technology selected for data transmission, and to ensure that in future the TTC network could be extended into the tracker volume and avoid constructing a second internal control network.

Both TTC and optical link R&D could be undertaken as common SLHC developments, in collaboration with other experiments.

2.6 Power

Power consumption will remain a major concern and significant effort will be needed to solve this problem; there are several reasons:

- Channel numbers are expected to increase, so any gains from reduced front-end power consumption will be at least partially offset,

- Power dissipation by silicon sensors will be a far more significant contribution than the 10% expected at the end of LHC operation; this has yet to be quantified,

- Amplifier noise is closely related to transistor current, and low noise performance usually requires relatively high currents in the input transistor. There will be gains from reduced sensor lengths, but increased logic will consume more power, as will operation at higher clock speeds, especially for driving data links. This can only be quantified by building prototype chips,

- The volume available for cables and cooling services is strictly limited, and already congested; thus cable resistances will not decrease. Any increase in total current, which is likely to result from lower ASIC supply voltages, will increase voltage drops, and heat losses, in power cables,

- Increased cable voltage drops and finer feature processes imply increased vulnerability to ASIC overvoltages. The absence of internal voltage regulation in present

tracker systems is one factor; another is the risk of short circuits and inductive ringing which could be induced,

- Future systems are likely to require more pixel layers. Present pixel systems have considerably higher power density ($5\,\text{kW/m}^2$) when compared to present microstrip systems ($0.4\,\text{kW/m}^2$),

- Both power provision and heat removal add to the material budget, in the form of cables and cooling pipes. It seems that material scaling with power consumption is not linear, so if overall power reduction can be achieved, it is likely to have significant benefits for physics.

Two innovations have been suggested which could help reduce the scale of the problem: serial powering and DC-DC conversion inside, or near to, the tracker. Serial powering would connect modules in series, requiring different modules to be operated at different DC voltage levels. This raises a number of important questions, of which noise performance is only one, and a few studies have begun [22, 23]. DC-DC conversion would allow power to be brought in at higher voltage, but so far there has been even less work to investigate such power conversion inside the detector.

2.7 System issues

It is widely recognised that the effort necessary for sensor and electronic R&D to achieve the unprecedented levels of radiation hardness and robustness required for the LHC, as well as manufacturability, was considerable. Less time was originally envisaged to address system-scale issues, such as manufacture and assembly of front-end hybrids, distribution of power and rationalisation of cooling schemes. In most cases, these issues required at least as much attention as that devoted to sensors and readout components. In addition, only when system scale tests were carried out was it possible to address small points which emerged during qualification.

There will be new issues to address in future, some of which will depend on the success with other developments, such as reduced power consumption. Operation at lower temperature, which seems likely to be needed, presents several challenges, for heat removal and provision of pipes and services, validation of structures and materials, and qualification of components, especially including circuit operation. Sufficient time should be allowed in schedules to allow for unexpected features to emerge and be corrected.

2.8 Triggering using the tracker

To make tracker data available for use in the Level-1 trigger presents a major challenge. The readout architectures of both the present pixel and microstrip systems are not compatible with delivering tracker hits to the trigger system even with an increased latency of $6.4\,\mu\text{s}$ and data would be required much earlier than this to allow time for processing. Transferring large volumes of data at high speed would require considerable power so it seems likely that some amount of data processing on the detector will be required.

Few tracking trigger concepts have so far extended to full track reconstruction, although should this be feasible it would certainly improve performance. One approach which might

achieve this makes use of associative memories, which has already been demonstrated in CDF [24]. It makes use of binary readout in the front end electronics, followed by transfer of the full granularity data off detector using optical links to dedicated processors which reconstruct tracks.

To limit the overall L1 rate it may be sufficient to associate track elements pointing to significant energy deposits in the ECAL, for example. Since even under high occupancy conditions it is not likely there will be a high rate of such track stubs pointing accidentally to triggering calorimeter towers, it could be enough to reconstruct elementary tracks in a small number of layers originating from a primary vertex compatible with a beam interaction. However, even this is far from simple, since it implies associating data from different tracker layers, and eliminating stubs from low transverse momentum tracks.

One promising concept uses two layers of small pixels in close radial proximity to each other so that coincidences of hits between the two layers amount to a track transverse momentum cut[25]. This reduces the large amount of low-momentum data while keeping the tracking efficiency very high for high-p_T tracks. The double layer could be located in the region between the present pixel system and the innermost microstrip layer. A practical design needs to be developed to study this idea more deeply.

Among the issues to be confronted are not only the challenges of building radiation hard pixel sensors and readout, but data transfer and processing on the detector and the power requirements.

2.9   Roadmap for a tracker upgrade

To develop more concrete proposals, the tracker proposes to set up a number of working groups with specific objectives, steered by a central team. At the top level, there will be definitions of targets for a new tracker, which will include outline specifications of:

- targets for cost, material budget and power consumption
- expected radiation level requirements and operational lifetime
- guide parameters for definition of outer tracker, including sensor material
- major operational conditions, such as temperature and need for analogue information

The proposed working groups will include the following subjects and major objectives:

- **Performance and detector layout**, to estimate the granularity needed to achieve the required tracking performance, to quantify the material budget breakdown and identify where significant gains could be achieved and potential improvements in physics performance, and using guidelines for likely cost, power and material budget, to define boundary between inner and outer tracker.
- **Sensor material and operation**, to recommend sensor material and expected sensor parameters for outer tracker, to investigate sensor options for inner layers and further R&D requirements, to define operating conditions for outer sensors, including bias voltage, expected leakage currents and environmental requirements, and investigate prototyping of outer tracker sensors with industry and recommend options to be pursued.

- **Outer tracker readout system definition**, to define requirements and specifications for a new readout chip, to investigate front-end connection and assembly technology, to identify requirements for TTC and control system, and optical links, and to outline a new readout and control system.

- **Pixel system and triggering**, to review the present system and advise what significant developments are needed to gain improvements in performance and lifetime, to recommend R&D on further pixel developments, to recommend sensor materials to be studied for inner layers and further R&D requirements, to define a readout system architecture and propose options for triggering layers.

- **Manufacture and material budget**, to investigate present and alternative cooling schemes to identify what systems are possible, to investigate new materials and support structures for prototype modules, and begin to prototype modules for improved ease, speed and reliability of assembly.

Chapter 3

SLHC Trigger and Data Acquisition

3.1 Introduction

The CMS trigger and data acquisition systems (TRiDAS) will need significant modifications to operate at the SLHC design luminosity up to ten times the LHC design luminosity of $10^{34} cm^{-2} s^{-1}$, possibly with a crossing frequency half the present 40 MHz. Due to the increased occupancy of each crossing, at the SLHC Level-1 trigger systems would experience degraded performance of the LHC algorithms presently planned to select the 100 kHz of crossings from the input rate of 40 MHz. The electron isolation algorithms would experience reduced rejection at fixed efficiency and the muon trigger would experience increased background rates from accidental coincidences. The DAQ system would experience larger event sizes due to greater occupancy. If new, higher channel-count trackers replace the existing ones, then the increase would be greater. This would reduce the maximum Level-1 trigger rate for a fixed readout bandwidth.

3.1.1 TriDAS Goals

The goal of the CMS SLHC TriDAS system is to enable a physics program with integrated luminosity roughly ten times that of the LHC or about 3000 fb^{-1}. The priority for the CMS SLHC TriDAS system is to capture the physics of the SLHC with the highest efficiency possible at acceptable detector readout and data storage rates.

3.1.2 TriDAS Performance

In order to meet the challenges of SLHC operation the suggested approach is to hold the overall Level-1 trigger rate at the LHC value of 100 kHz while increasing the readout bandwidth. This approach avoids rebuilding front-end and readout electronics as much as possible since these were designed for an average readout time of less than 10 μsec. It also permits use of front-end buffers for an extension of the Level-1 Accept (L1A) latency rather than for more post-L1A storage before readout. However, maintaining a 100 kHz L1 rate at the SLHC will increase the burden on the DAQ, which will need to transport more than the anticipated LHC data size of 1.5 MB per event.

Holding the L1 trigger rate to 100 kHz at the SLHC without improving the trigger system implies raising E_T thresholds on electrons, photons, muons and jets, as well as the use of less inclusive triggers. Such strategies assume that with several years of data accumulated at the LHC design luminosity before the SLHC upgrade, many of the physics studies requiring lower E_T thresholds would have sufficient statistics and that the physics at the LHC would

be sufficiently understood to provide the necessary understanding of efficiency for more complex trigger configurations. However, for searches for rare events or attempts to make precision measurements, there would be a substantial loss of physics performance.

3.1.3 TriDAS SLHC Strategy

The goal of the upgrade of the CMS TriDAS is more than mere survival during SLHC operation with a comparable performance to that experienced at the LHC. The intent is to exploit the evolution in technology since the original TriDAS design and the experience of building and operating CMS at the LHC. The purpose is to significantly enhance the capabilities of the SLHC TriDAS to provide a level of performance that ensures the greatest access to physics signals.

The strategy for upgrading TriDAS follows the present strategy of TriDAS evolution during LHC running of first operating any hardware Level-1 (L1) trigger virtually in the Filter Farm Higher Level Trigger (HLT) code using emulation compared with the data read from the L1. During the first phase of CMS LHC operation, the L1 algorithms involve data from the calorimeter and muon systems. Once the reduction power of these subsystems is fully exploited, the next step is to use tracking information. It is our intent to explore using the tracking information in the L1 to further extend the physics reach of CMS for the SLHC.

3.2 SLHC Trigger Operation

3.2.1 Crossing Frequency

The operation of the SLHC at a bunch crossing frequency of 20 MHz instead of 40 MHz will increase event pile-up, decrease trigger algorithm performance, and increase data volume for detectors that have time resolution sufficient to identify data associated with individual 25 ns bunch crossings. Since running the SLHC at 40 MHz will be retained as an option to mitigate these difficulties, we require that all CMS detector and electronics designs for SLHC upgrades work with a 25 ns bunch spacing and handle an occupancy consistent with $\mathcal{L} = 10^{35}\,\mathrm{cm^{-2}\,s^{-1}}$ at 50 ns bunch spacing.

Operating the SLHC with 50 ns bunch crossing spacing at $\mathcal{L} = 10^{35}\,\mathrm{cm^{-2}\,s^{-1}}$ implies a pileup 300-400 min-bias events/crossing. This is a factor of more than 20 greater than the LHC design luminosity ($\mathcal{L} = 10^{34}\,\mathrm{cm^{-2}\,s^{-1}}$) figure of 17 min-bias events per crossing and will degrade all occupancy-dependent trigger algorithms that rely on forms of isolation to identify electrons, muons, τ's and missing energy signals. It will require a more performant trigger with additional information, such as tracking data, used to reduce the trigger rates against the much higher backgrounds. The size of regions sampled for trigger decisions will need to shrink to handle the increased backgrounds. These are the guiding principles upon which we base our design of the SLHC trigger system for CMS.

3.2.2 Trigger Thresholds

The CMS trigger system has three major physics performance requirements. First, it needs to be efficient for high-p_T discovery physics. If the raising of thresholds results in a substantial

rate reduction, then this can be satisfied since these thresholds are high. Second, it needs to provide high statistics for topics such as precise measurements of the Higgs sector. This requires low thresholds on leptons, photons and jets. This can be partially addressed by the use of more exclusive triggers rendered usable by the previous years of understanding final states observed at the LHC, provided that the introduction of these more exclusive triggers does not introduce more systematic errors. Finally, the trigger needs to provide control and calibration triggers. These are samples such as W, Z and top events. Such low-threshold triggers can be held to an acceptable rate by prescaling, which should yield more than adequately sized samples.

An initial study of the L1 trigger thresholds for the SLHC [11] suggested inclusive single muon and electron thresholds of 30 and 55 GeV with rates of 25 kHz and 20 kHz respectively. Electron and muon pair thresholds of 30 and 20 GeV respectively are predicted to have rates of a few kHz, from present LHC-based simulations. Inclusive Jet and missing E_T thresholds of 350 and 150 GeV, respectively would produce rates of about 1 kHz. The actual triggers employed might use less inclusive conditions to access lower thresholds.

3.2.3 Trigger Rates

The ability of the CMS L1 trigger to reach the above SLHC performance goals depends on many factors and is not necessarily achievable with the present LHC trigger information. An example is the CMS muon trigger. The single muon trigger rates as a function of the p_T threshold are shown in Figure 3.1 for LHC design luminosity ($10^{34} cm^{-2} s^{-1}$). The rates are shown separately for Level-1 (L1 Trigger information only), Level-2 (HLT reconstruction using full-resolution muon system data only with isolation calculated from full-resolution calorimeter data), and Level-3 (HLT track momentum and isolation calculated from silicon strip and pixel tracking data), with and without isolation applied at Levels 2 and 3. Also shown is the single muon rate that was generated in the simulation. A threshold of 31 GeV/c reduces the single-muon Level-3 rate to 50 Hz with isolation (100 Hz without isolation).

In Figure 3.1 the Level-2 rates have a reasonable reduction with increasing muon p_T cut up to 20 GeV/c, where the rate is 200 Hz. Above a p_T of 20 GeV/c, the reduction of rate with increasing muon p_T cut is very slow, dropping only a factor of 2 with an increase in p_T cut up to 60 GeV/c. Therefore if we bring the full Level-2 algorithm performance (without tracking) to bear in Level-1 for the SLHC, this shows that above a p_T threshold of 20 GeV/c the only effective method to reduce the rate with increasing threshold is to use Level-3 algorithms which involve tracking. This motivates examining the use of tracking information in the SLHC L1 trigger.

3.3 SLHC Trigger Primitives

3.3.1 Calorimeter and Muon Triggers

The existing trigger primitive information used by the L1 trigger systems needs examination to determine adequacy for use at the SLHC. For the calorimeter, the forward quartz fibre detector might require finer-grained information to provide a smaller trigger tower size. The HCAL and ECAL have sufficient time and spatial resolution for SLHC operation, using their present 40 MHz sampling without significant modification. However, replacement of the
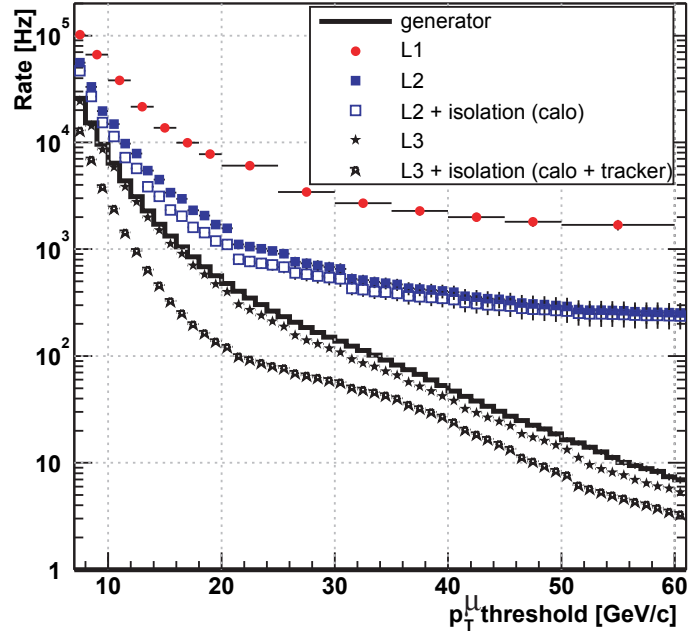
Figure 3.1: The HLT single-muon trigger rates as a function of the p_T threshold for a luminosity of $10^{34} cm^{-2} s^{-1}$. The rates are shown separately for Level-1, Level-2, and Level-3, with and without isolation applied at Levels 2 and 3. The rate generated in the simulation is also shown[26].

endcap ($1.5 < \eta < 3$) calorimetry may be needed due to radiation damage. The muon system RPCs may not function at the SLHC luminosity, particularly at high η in the endcap. The barrel muon system drift tube chambers could continue to operate at 40 MHz, with increased backgrounds that could be reduced by combination with a Level-1 tracking trigger. The endcap muon system Cathode Strip Chambers will probably continue to be usable for triggering at the SLHC with some improvements and higher thresholds.

3.3.2 Tracking Triggers

A possible source of trigger primitives not presently used in the CMS L1 trigger system is the tracker. This would most likely require a replacement of the tracking system and a change in technological solution. However, it is also likely that operation at the SLHC will require replacement of the CMS tracking system. Therefore it is worth considering what form tracking trigger primitives might take and how they might be used. CMS has the provision for a type of L1 tracking trigger using the z-vertex positions of pixel clusters of high hit occupancy in $\Delta \eta \times \Delta \phi$ bins. This could be used to reject jets from pile-up events since their z-vertices would not line up with the main event z-vertex. At present the logic for this is not implemented, but retained as an option.

A L1 tracking trigger could provide either an inner track or an an outer stub or both. These would be used to combine with the calorimeter at L1 to reject π^0s and reject jets from pileup. They would be used to sharpen p_T thresholds and reduce accidentals and wrong crossing determinations in the muon system. Implementation would not only require rebuilding the tracker, but also rebuilding the calorimeter and muon trigger systems to various degrees in order to provide outputs with suitable granularity and other information to combine with

the L1 tracking trigger.

3.4 SLHC Trigger Algorithms

3.4.1 Tracking Trigger Algorithms

The most effective method to significantly improve trigger functionality for the SLHC may be to employ tracking at the earliest stage possible. In order to evaluate the effect of tracking on trigger performance, it is appropriate examine how tracking is first used in the CMS HLT since these are the candidate algorithms for migration to L1. As an example, attaching tracker hits to muon tracks improves the p_T assignment precision from 15% for the endcap muon system stand-alone to 1.5% with the tracker information included [26]. This also improves the sign determination and provides a vertex constraint. In addition, pixel tracks are found within a cone around the muon track and their sum p_T is used as an isolation cut. This is less sensitive to pile-up than calorimetric information if the primary vertex can be determined. The combination of tracking p_T resolution and isolation provide more than an order of magnitude reduction in the CMS L1 muon trigger rate as shown in Figure 3.1. To implement such a trigger at L1 in CMS would require information on muon track locations on a $(\eta \times \phi) = (0.0125 \times 0.015)°$ for adequate correlation with the tracker. While finer than the present CMS $(\eta \times \phi) = (0.05 \times 2.5)°$ trigger scale, this information is already available but not used.

Tracking information in the HLT also reduces the CMS L1 calorimeter trigger rate. As shown in Figure 3.2 the correlation of an electron trigger with an extrapolated pixel track reduces the rate by a factor of 10. Tracking information is also used as an isolation cut for photon candidates. As shown in Figure 3.3 the CMS L1 calorimeter jet-based τ-lepton trigger is also reduced a factor of 10 by requiring isolation using pixel tracks outside the signal cone and inside an isolation cone. In order to use the information from a tracking trigger at L1, the calorimeter trigger e, γ and τ objects could be used to seed tracks with the full calorimeter trigger tower $0.087\eta \times 0.087\phi$ granularity. Candidates could be pre-sorted and limited to a maximum number, such as 32. A single track match within a 3×3 trigger tower region with a coarse p_T resolution (8-bit scale with 1 bit/GeV) could be sufficient to reduce the electron trigger rate. A veto of tracks in the 3×3 trigger tower region would be used for a veto of photon candidates and a single or triple track match would be used for τ candidates.

3.4.2 CalorimeterTrigger Algorithms

Other upgrades to the CMS calorimeter trigger would be needed to reduce the jet and missing energy trigger rates. These would include allowing the clustering of jets in multiples of 2×2 trigger towers: 6×6, 8×8, 10×10 with a sliding window making one or two tower steps and use of higher resolution scales with more precise geometry for missing energy. All of these changes to the CMS L1 calorimeter trigger would represent a reasonable extension of the present system. Technological advances in FPGAs and data links would permit processing of high speed serial data such as 32 10 Gbit/s links per card with high speed serial output in the 4–10 Gbit/s range.
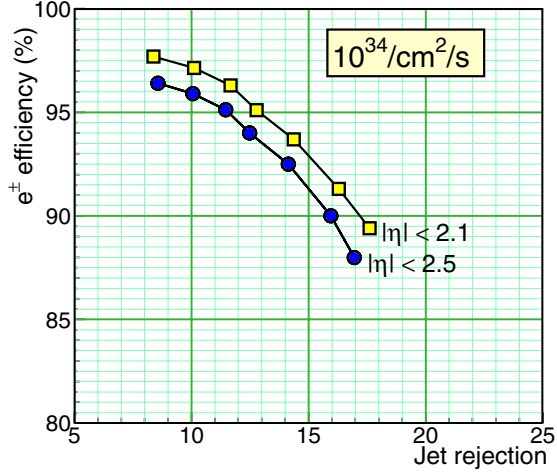
Figure 3.2: Electron HLT rejection versus efficiency[26] from pixel matching for a luminosity of $10^{34}cm^{-2}s^{-1}$ for two different η ranges.
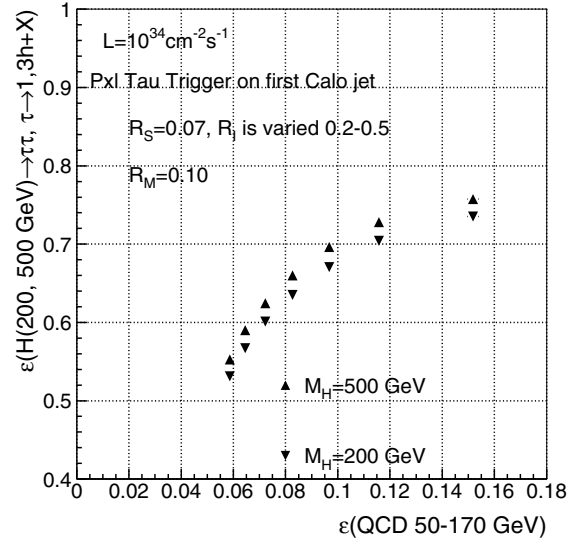

Figure 3.3: Efficiency of pixel track τ HLT[26] cut on the first calorimeter jet in $A^0/H^0 \rightarrow 2\tau \rightarrow 2\tau - jet$, for two Higgs masses, M_H = 200 and 500 GeV/c^2, versus the efficiency for QCD di-jet background events for a luminosity of $10^{34}cm^{-2}s^{-1}$.

3.5 SLHC Trigger Architecture

The implementation of the new algorithms involving tracking discussed above would require modification of the CMS trigger architecture. The present CMS architecture provides a flow of data within the muon and calorimeter trigger systems from regional to global components into the CMS Global L1 trigger. For the SLHC the L1 trigger data would need combination between tracking and calorimeter and muon triggers at a regional level with finer granularity than presently employed. After this regional correlation stage, the physics objects made from tracking, calorimeter and muon regional trigger data would be transmitted to the global trigger. The important new feature is that some of the tracking, isolation, and other regional trigger functions would be performed in combinations between regional triggers in a new hardware layer composed of regional cross-detector trigger crates as shown in Figure 3.4. An advantage of this architecture is that it would leave the present CMS L1 and Higher Level Trigger (HLT) structure intact by not adding additional trigger levels. This minimizes the impact on the CMS readout.

3.6 SLHC Trigger Latency

The additional layer of processing for combination of tracking information, increased algorithm complexity and larger trigger data volume due to finer trigger granularity suggest an extension of the present CMS 3.2 μs L1 latency. A longer latency would also be needed for use of FPGA embedded serializers and deserializers, the addition of more serialization and deserialization steps to use high speed serial links or the use of buffers to incorporate commercial serial links running asynchronously with respect to the LHC clock. The CMS

L1 latency is limited by th̶
electronics. Since it is exp
sonable to assume that th
r̶ved. The next limit̶
corresponding to time of 6.4 μs. This is proposed as the CMS SLHC L1 latency baseline.

Trigger Primitives

Tracker L1 Front End

e / γ/ τ clustering
2x2, ϕ-strip 'TPG'

μ track finder
DT, CSC / RPC

Regional Track
Generator

Jet Clustering   Missing E_T Seeded Track Readout

Regional Correlation, Selection, Sorting
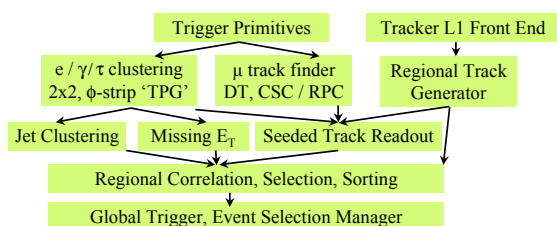
Global Trigger, Event Selection Manager
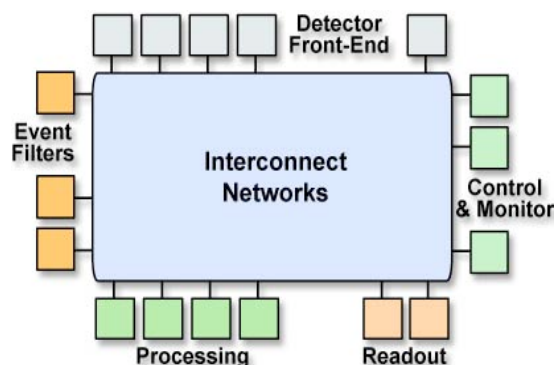
Figure 3.4: Schematic of SLHC L1 Trigger

Figure 3.5: Schematic of SLHC DAQ

3.7 SLHC DAQ

If one assumes that the L1 trigger rate remains at 100 kHz, the increased channel occupancy and finer detector granularity, leading to a larger channel count, suggest a bandwidth requirement for SLHC DAQ systems at least 5–10 times that of the present LHC DAQ systems. The present CMS LHC DAQ uses a network with Terabit/s aggregate bandwidth constructed from two stages of switches and a layer of intermediate data concentrators for optimizing traffic load to the event builder. The capacity of the buffer memories of 100 GB between the front-end readout and the event builder permit a real-time DAQ latency of seconds. A proposed architecture for the CMS SLHC DAQ is shown in Figure 3.5. The concept is to incorporate as much of the DAQ functionality as possible into a commercial network of the capability one can expect from industry in the next decade. This incorporates a scalable multi-Terabit/s network to interconnect all of the elements. The function of the Event Manager (EVM) is incorporated into the L1 trigger. The EVM updates the list of available event filter services where events are to be sent for processing. Along with the L1 accept, the trigger transmits additional information to the front ends including the event type for post L1 processing and the destination address of the event filter node where the resident event fragment is to be transmitted. This requires the control logic to process and transmit instructions at the 100 kHz L1 trigger rate to every readout, trigger event filter and other element of the DAQ. The event fragment delivery and the event building itself are provided by the network protocols using the commercial network hardware. This design allows for real-time buffers consisting of Pbytes of storage disks, which would permit storage of events being processed over a period of days. This opens up the possibility of using non-local compute nodes and GRID tools to maximize access to remote resources in a flexible manner.

3.8 SLHC Trigger and DAQ R&D

3.8.1 TTC Upgrade

An important development needed for the proposed CMS SLHC DAQ architecture outlined above is a revision of the existing Trigger Timing and Control (TTC) system [27] to transmit and receive added fast control information. A new TTC system should provide the clock, L1 Accept, Reset, Bunch Crossing 0 and trigger type information in real time for each crossing. An R&D effort would be required for the TTC system clock signal so that it can meet the jitter and other requirements to drive the new generation of high-speed serial links, as well as to be capable of functioning at the GHz frequency needed to meet the fast message distribution needs of SLHC trigger and DAQ.

3.8.2 Trigger Upgrade

The substantial increase in algorithm complexity and volume of data they need should be met by industrial development of Field Programmable Gate Arrays (FPGA) promising faster devices with higher logic gate counts and increased I/O. Of note is the advent of embedded GHz serializers and deserializers on the FPGA inputs and outputs, enabling very high throughput. However, the latency of these circuits remains a concern and needs study. The use of these devices is becoming more challenging in terms of packaging, routing, mounting, and low supply voltages. R&D on the use and performance of these rapidly evolving devices is needed for development of SLHC trigger systems. The need to move larger quantities of data at higher speed requires R&D on high-speed serial links and backplane technology, as well as on the clocking systems needed to drive them with low jitter.

3.8.3 DAQ Upgrade

The CMS DAQ system will also depend on are progress in backplane and data link and technologies, which will need to incorporate the newer frequency 40 GB links and protocols. The much tighter integration of front-end electronics and the links needed suggests that R&D on these be done together. The front-end electronics will have many challenges for R&D to handle the increased processing and channels counts. While improvements in VLSI technologies should provide the necessary compnents, there are many R&D issues such as radiation tolerance, power reduction, system complexity, and integrating the commercial data communications developments. The DAQ system itself also faces a considerable challenge of managing the complexity caused by the increasing numbers of components, operations and stages of processing. This will require R&D on more sophisticated controls and diagnostics. One path is to exploit as much as possible industrial developments in this area.

As the L1 trigger adopts more sophisticated algorithms that were formerly used in the HLT and the backgrounds for the HLT algorithms increase, the HLT algorithms must become much more sophisticated. This will require considerable study, physics simulation and eventually physics analysis of LHC data to determine the initial set of HLT algorithms.

Chapter 4

Upgrades to the Calorimeters

The barrel regions of the electromagnetic (ECAL) and hadronic calorimeters (HCAL) are designed to be able withstand the expected integrated luminosity at the SLHC. There should be no need to replace on-detector electronics, although changes may need to be made to off-detector electronics to accomodate a new trigger and DAQ system. The endcap calorimeters may suffer reduced performance at high luminosities, but the on-detector electronics should continue to operate.

4.1   ECAL issues for SLHC

4.1.1   ECAL detectors

The ECAL Lead-Tungstate crystals have been tested to radiation doses in excess of what is expected for running at the SLHC, and are expected to operate well, albeit with somewhat reduced light yield. The VPTs in the endcap calorimeter may also suffer some darkening at very high luminosity. The efficiency of the PreShower detector which lies in front of the ECAL endcaps will need to be studied, although the detectors and electronics should in principle continue to operate.

4.1.2   ECAL electronics

The front-end electronics for the ECAL is mounted directly behind the crystals and is not easily servicible. All components have been tested to operate at radiation levels exceeding that expected at the SLHC. The most sensitive components are the low voltage regulators, which may require running at a higer voltage drop in order to operate after very high integrated fluences. This is most likely to affect the endcap as the barrel will see a significantly lower fluence.

The front-end digitization will continue to operate at 40 MHz. This is not a problem for machine operation at 20 Mhz as the full signal waveforms are digitized and transmitted offline where bunch identification can be determined. The ECAL also forms trigger primitives for the Level-1 calorimeter trigger, and the electonics has provided for the ability to calculate trigger primitives for two bunch hypothesis at each 40 MHz sampling, hence be able to provide trigger information even for 80 MHz running should this option reemerge.

Off-detector electronics would need to be updated to cope with any changes in the bunch crossing frequency, and changes to the Trigger and DAQ system.

4.2   HCAL issues and R&D for the SLHC

The HCAL is largely immune to the changes in environment between the LHC and the SLHC. There are several issues that are of concern and are being addressed. The high eta region of the endcap hadronic calorimeter (HE) will suffer extensive radiation damage requiring replacement. Improvements in MIP recognition in the outer hadronic calorimeter (HO) will be desirable. The main issue with the forward hadronic calorimeter (HF) is whether it will be obstructed by elements of the machine placed inside CMS which could potentially be required to increase the focusing strength of the machine. Finally the HCAL readout electronics may need to accommodate higher bandwidth in readout and finer trigger primitive granularity. These issues are being studied.

4.2.1   Radiation resistant replacement for high eta HE scintillator

In the current design, HE uses plastic scintillator tiles and wavelength shifting fiber. These materials have been shown to be moderately radiation hard up to the 2.5 Mrad limit expected from 10 years operation of the LHC. Under the SLHC conditions the lifetime radiation dose at the HE will increase from 2.5 Mrad to 25 Mrad. The scintillator tiles used in the current design of HE will lose their efficiency due to high radiation. They will need to be replaced with new devices that fit in the geometric constraints of the existing absorber structure. There are several possible solutions, including new scintillators, parallel plate chambers, silicon sensors, and quartz plates that generate cerenkov light rather than scintillation light. Each of these solutions is being pursued.

Parallel plate chambers (PPAC) have been built and tested. Large-area PPACs have a thin HV plate suspended between two grounded plates. A PPAC can be made entirely of metal and ceramic that is unaffected by radiation. Aging is prevented by suitable additives to the gas and by gold plating the inside surfaces. The output signals are connected directly to $50\,\Omega$ coaxial cables so that all of the electronics can be located in a remote region.

Another alternative is to replace the scintillators with quartz plates and the waveshifting fibers with rad-hard (possibly inorganic) waveshifter. The cerenkov light produced in the quartz is about 100 times less than scintillation light. One goal of this R&D is to optimize the light yield by careful placement of the wavelength shifters. The HCAL group has built small calorimeters based on this technology and tested them in the CERN testbeam during the Fall of 2006.

Silicon sensors are also being considered as a replacement. This idea would use radiation resistant silicon sensors to replace the scintillators in the highest eta region of HE.

4.2.2   New photodetectors

The HCAL community is actively studying silicon photomultipliers (SIPM) as a replacement for the HPDs in the HO region. It is estimated that SIPMs can give a factor of 4 larger signal to noise, which is critical for improved muon and MIP identification with this detector. A prototype readout using SIPMs has been built and tested in CERN testbeams in September 2006.

4.2.3 New electronics

The HCAL group is engaged in design studies of new electronics to improve the readout speed into the DAQ, and improve trigger granularity for future enhanced calorimeter triggers.

Chapter 5

Upgrades to the Muon Systems

5.1 R&D topics for the SLHC

The CMS muon detector is composed of Drift Tube chambers (DT) in the barrel region, Cathode Strip Chambers (CSC) in the end-cap regions, and Resistive Plate Chambers (RPC) both in barrel and in end-caps.

They have been designed for tolerating hit rates a few times larger than those expected at LHC. They seem still adequate for use at the SLHC, although more R&D work and LHC data is necessary to substantiate that possibility, in particular for the larger rapidity regions.

Major upgrades will definitely be needed for front-end electronics, trigger and readout electronics, in order to cope with the larger hit rate and to tolerate the SLHC radiation background and, for digital electronics, to adapt to the SLHC bunch crossing frequency.

The CMS RPC system is divided in two regions: barrel ($0 < \eta < 1.2$) and endcap ($0.9 < \eta < 2.4$). The expected total rate goes from 5–10 Hz/cm^2 of the barrel region up to 1 kHz/cm^2 of the very forward endcap region ($\eta > 2.0$).

RPC chambers have been exposed at the GIF gamma radiation for more than two years in order to study possible long-term ageing effects. A total charge corresponding to about 15 CMS years has been integrated during the GIF test.

During the test no significant degradation of the chamber performances has been observed and after 15 CMS years the noise rate was less than 5–10 kHz/cm^2 . The rate capability of about 1 kHz/cm^2 was stable during the test.

The front-end electronics has been tested with neutrons at reactors showing an average time between SEU events of less than 1 h/RPC corresponding to a rate of *false* hit < 0.27 mHz. At the GIF the front-end electronics showed a very stable performance during the whole test.

Assuming backgrounds at the SLHC were a factor 10 higher than the LHC, the CMS RPC chamber and their electronic front-end are potentially adequate for use in the barrel region and in the first end-cap station. More difficult is the larger rapidity end-cap region where the estimated background at the LHC is 0.1–1.0 KHz and where an additional factor 10 on the background would bring the operation very close to the RPC rate limit.

In the DT system, on-chamber trigger electronics uses a mean timing technique for generating a *high-p_T muon* signal for triggering CMS on a LHC specific bunch crossing (BX). Operation at 20 MHz as opposed to the current 40 MHz should not represent a problem.

In the CSC system, the ME4/2 chamber has to be restored for the reliable trigger operation

and improved coverage. It is expected that the ME4/2 will be recovered by ≈ 2009–2010 for the nominal luminosity phase of the LHC, before the SLHC. It has been shown that the electronics, as currently designed, would function at the SLHC. However this statement should be reevaluated after experience with data proper. We do wish to evaluate whether any of the on-chamber electronics would benefit from advances in technology that have taken in place sufficiently to warrant replacement. Given that the trigger and timing will be significantly different in the SLHC era (for example an asynchronous trigger is a real possibility), it is highly likely that we will replace elements of the trigger path. The Clock and Control Board will inevitably be replaced. Exactly which boards should be redesigned and what the elements of the new design should be the subject of study.

The DAQ path in CSCs may benefit from modifications. In particular the FED crate makes use of the TTC chip and may consequently need modification or replacement. Whether other elements of the DAQ path would benefit replacement will be studied. The adequate radiation tolerance of the on-detector electronics has to be confirmed in realistic beam conditions and with realistic shielding, and depending on the result, R&D on developing more radiation tolerant electronics may become necessary.

Chapter 6

Project Office

Implementation of the CMS SLHC Upgrades will be coordinated through a SLHC Project Office. This will be closely integrated with the CMS Technical Coordination.

6.1 Changes to the Infrastructure

One of the key areas which the project office will have to coordinate are any changes to the CMS interaction region which are mandated by changes in the machine. These may include changes to the insertion triplets which require substantial changes to the rotating shielding and TAS, as well as potential placement of magnetic elements into the UXC55. Either of these changes may require significant changes to shielding, beampipe design, and the detectors in the forward regions of CMS

Also important will be understanding how to remove and replace the first generation CMS detectors which will potentially be highly activated.

The project office will keep the schedule of upgrade work, and organization of installation and infrastructure work.

6.2 Organization of Upgrade work

The CMS Management and Collaboration Boards approved the creation of a CMS SLHC Upgrade Steering Group [28] in September 2005. This group was mandated to:

- recommend R&D proposals for approval to CMS Management and Collaboration Boards,
- plan SLHC workshops,
- provide outreach to collaboration on SLHC activities,
- interact/Co-ordinate with Machine and ATLAS on SLHC matters,
- report regularly to the CMS Managment and Collaboration Boards.

Each CMS sub-detector has nominated a representative to this steering group, and these act as liaisons with their detector upgrade communities. The current process for R&D proposals is for sub-detector groups to discuss the potential upgrade R&D, and then submit a proposal to the steering group. These proposals will be reviewed by the steering group and then recommended the the CMS Management and Collaboration Boards. It is expected that proposals will be summarized for the LHCC.

In order to prepare for and focus the needed R&D efforts, a series of four upgrade workshops have been held[19]. These workshops have been used to understand the potential upgrades, identify key areas of research, and communicate information on the upgrades to the CMS community. Future workshops will focus on identifying potential common R&D (across LHC experiments), and more detailed planning of upgrades.

6.3   Cost Estimates and timescales

Preliminary estimates of the costs for the expected upgrades are shown in table 6.1. These estimates are for the material costs, and do not include personnel. The estimates are based on extrapolation from the costs of the current CMS detector components, and assume a complete replacement of the tracking and trigger systems.

Table 6.1: Estimated costs for SLHC detector upgrades (in Million Swiss Francs). These costs are for materials only for construction, and do not include R&D costs

Sub-Detector	Estimated Cost
Inner Tracker	30 MCHF
Outer Tracker	90 MCHfF
Level-1 Trigger	20 MCHF
DAQ	10 MCHF
Muons and Calorimters	10 MCHF
Infrastructure	15 MCHF
Total	175 MCHF

The CMS strategy for upgrades is planned to proceed in two steps. In the first step, new layers of tracking detector which contain some elements capable of participating in the Level-1 Trigger decision are inserted within the volume of the current Pixel detector. These would be used as a technology demonstrator of the key elements of the upgrade, and could be installed during a normal winter shutdown.

The second step of the upgrade would be full replacement of the tracking and trigger systems, as well as required upgrades to the DAQ and other systems. This step would most likely take more than one year and would take place during the long shutdown required for upgrading the LHC machine. A notional timeline for the upgrade steps can be seen in Figure 6.1.
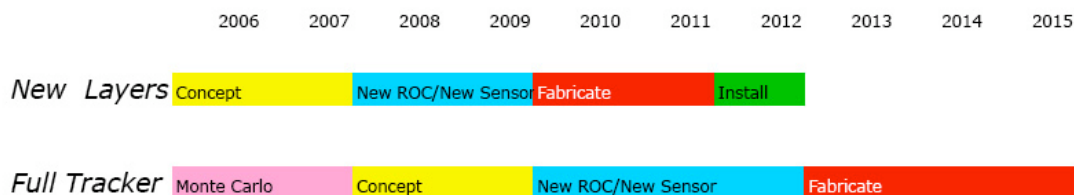

Figure 6.1: Timeline for steps involved in the tracker upgrade.

References

Notes:

a) CMS Notes are available at `http://cms.cern.ch/iCMS/` unless otherwise noted.

b) References marked **doi** should be prefixed with `http://dx.doi.org/`.

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