



ELECTRONICS AND PHOTONICS TECHNOLOGY OFFICE



We invite you to participate!

***Workshop on Supporting Technologies for
Semiconductor Lithography***

November 16, 1999, 2:00 pm – 6:00 pm

**DoubleTree Hotel
2050 Gateway Place
San Jose, California**

**What should ATP funding priorities be?
How can you successfully compete in ATP competitions?
How can ATP make a difference in your technology goals?**

Purpose of Workshop

- Overview of technologies for dimensions of 100 nm and beyond.
- Explore ATP's role in furthering U.S. leadership in next generation lithography technologies.
- Review the ongoing projects.
- Review ongoing NIST Metrology projects.
- Discuss ATP project selection process.

For further information contact:

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Attachments: Agenda for Lithography Workshop, Agenda for Microelectronics Manufacturing Workshop, Summary of Ongoing Lithography projects funded by ATP



**ATP National Meeting
November 15-17, 1999**

<http://www.atp.nist.gov/nationalmeeting>

**We invite you to attend ATP Proposers' Conference
and Technology Showcase**