



1999 ATP National Meeting, November 15-17, 1999

http:/www.atp.nist.gov/nationalmeeting/

Microelectronics Manufacturing Infrastructure

We invite your participation and input!

■ Discuss opportunities for future ATP EPTO partnerships

Assist American companies in competing in future ATP electronics competitions

Review activities of ATP projects and NIST supported metrology R&D

In cooperation with ...



Contact:

Make plans to attend the ...

Microelectronics

Manufacturing Workshop

November 15-16, 1999

DoubleTree Hotel, San Jose, CA

We encourage your involvement ...

Help explore the technical barriers confronting electronics manufacturing for the 21st Century and update ATP EPTO technology priorities.

Explore highest priority technical barriers confronting electronics

manufacturing, e.g. cooling; interconnect technology; extending the limits of CMOS technology; RF; factory integration; modeling, simulation & design tools; environmentally sustainable electronics

We invite you to attend ATP's Proposers' Conference and Technology Showcase

NST

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